

FIG. 1

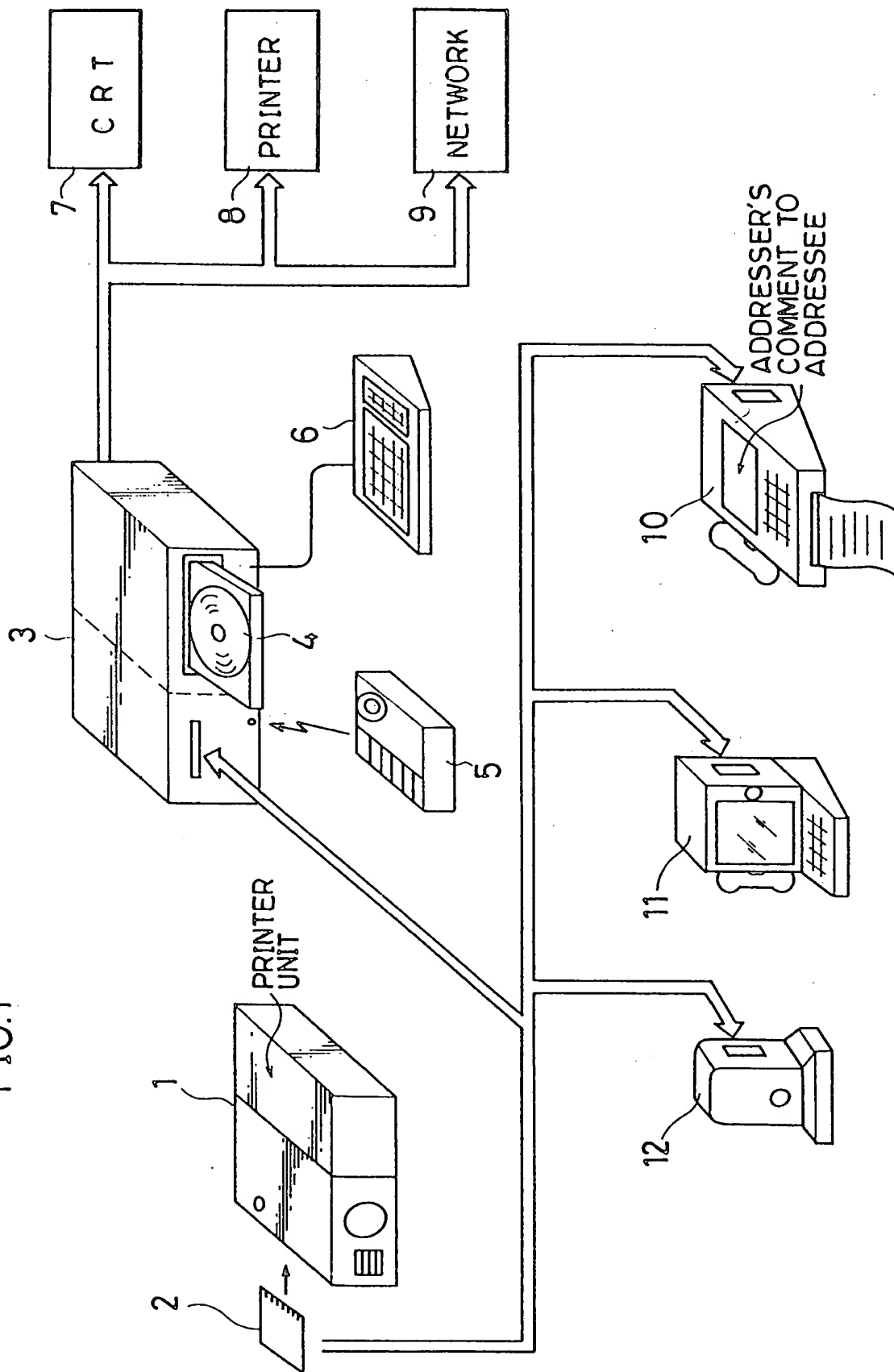


FIG.2A

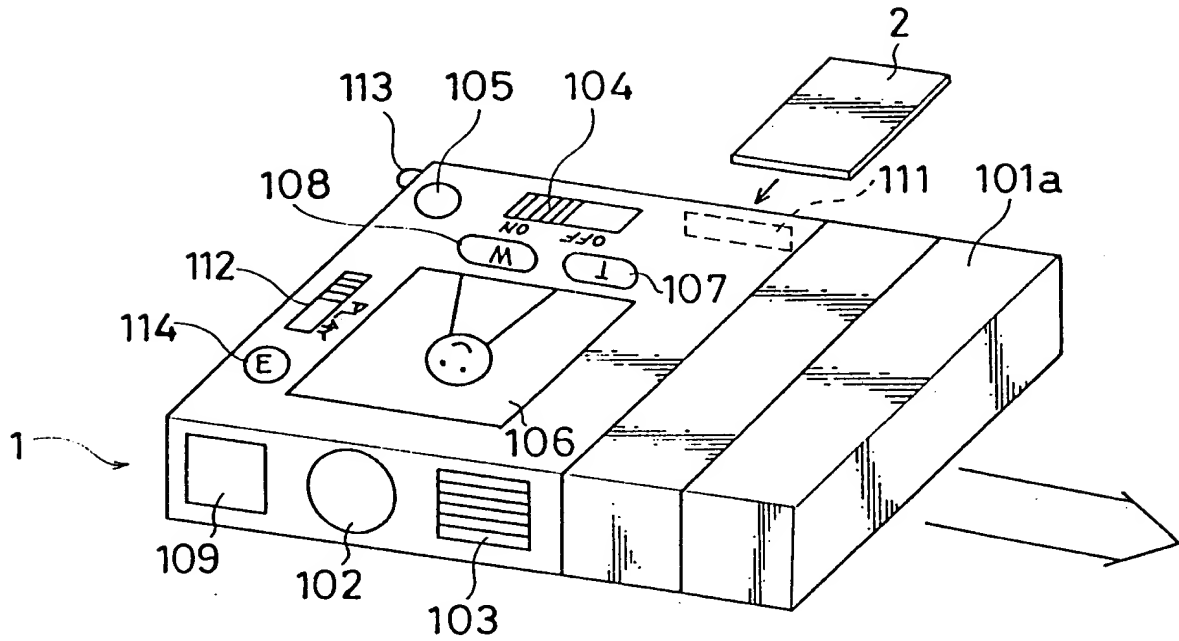


FIG.2B

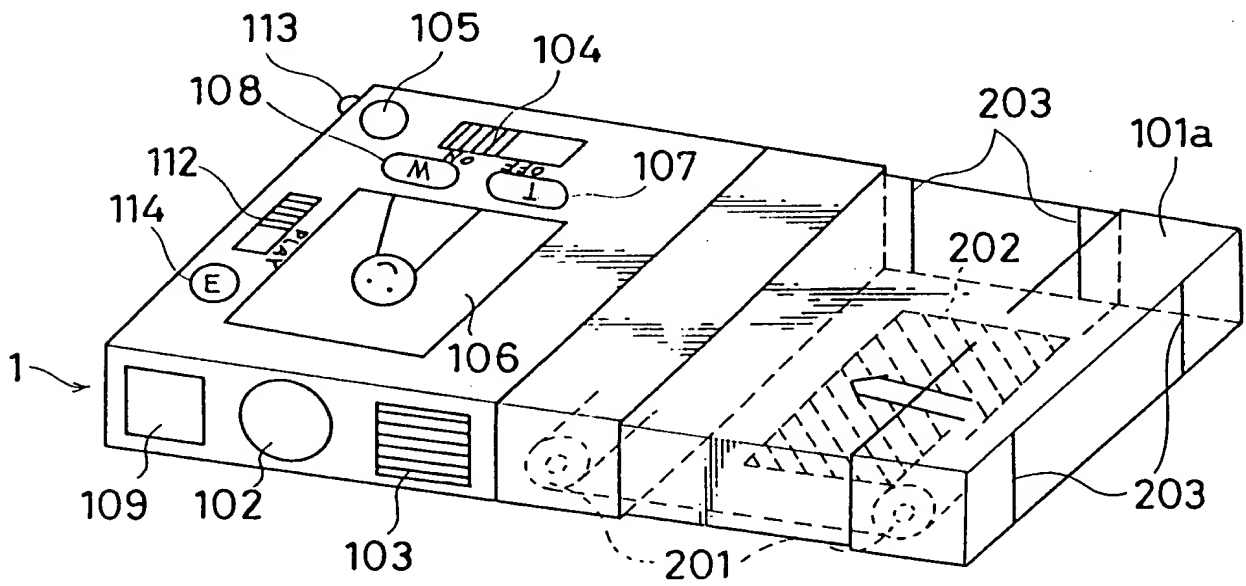


FIG. 3

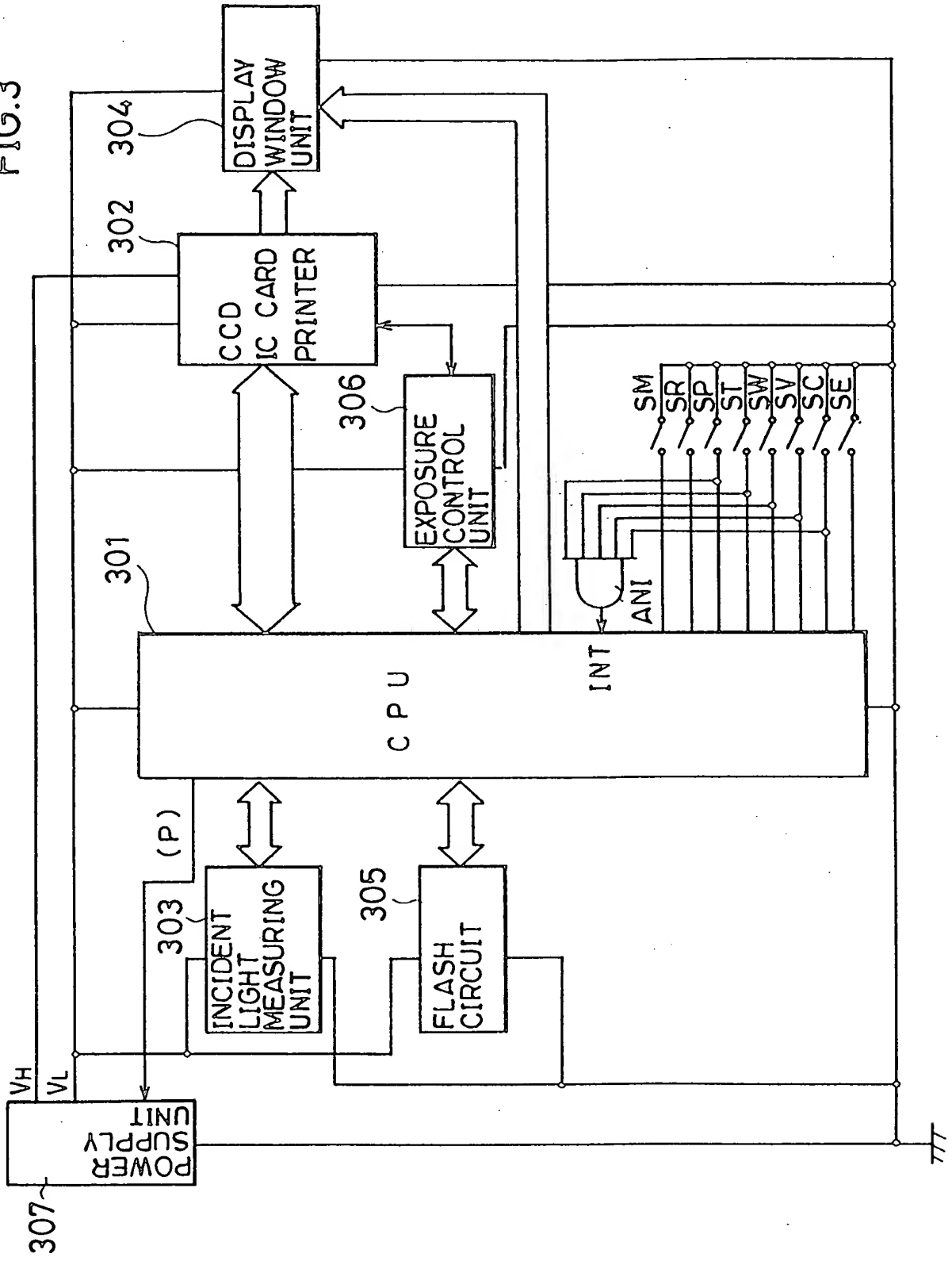


FIG. 4

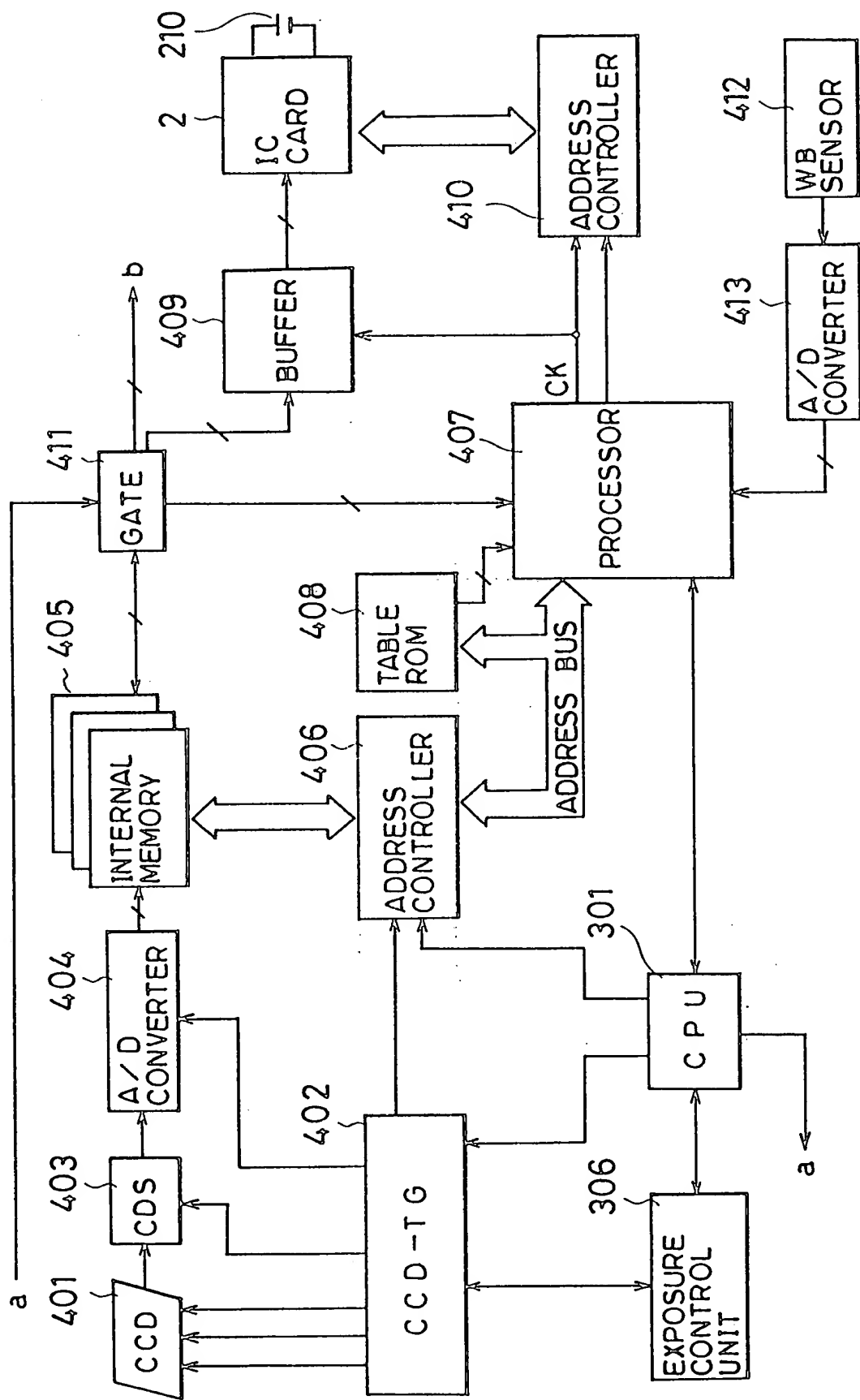


FIG. 1 is a block diagram of a data processing system. The system includes the following components and their interconnections:

- IC CARD (2)**: Connected to **PROCESSING WORK MEMORY (416)** via a bidirectional arrow.
- PROCESSING WORK MEMORY (416)**: Connected to **ADDRESS CONTROLLER (414)** via a bidirectional arrow.
- ADDRESS CONTROLLER (414)**: Connected to **PROCESSOR (415)** via a bidirectional arrow.
- PROCESSOR (415)**: Connected to **TABLE ROM (417)** via a bidirectional arrow.
- PROCESSOR (415)**: Connected to **LINE-BY-LINE SEQUENTIAL ACCESS MEMORY (418)** via a bidirectional arrow.
- LINE-BY-LINE SEQUENTIAL ACCESS MEMORY (418)**: Connected to **BUFFER (419)** via a bidirectional arrow.
- BUFFER (419)**: Connected to **PRINT HEAD (420)** via a bidirectional arrow.
- ADDRESS CONTROLLER (414)**: Connected to **CPU 301** via a bidirectional arrow.
- PROCESSOR (415)**: Connected to **CPU 301** via a bidirectional arrow.
- TABLE ROM (417)**: Connected to **CPU 301** via a bidirectional arrow.
- LINE-BY-LINE SEQUENTIAL ACCESS MEMORY (418)**: Connected to **CPU 301** via a bidirectional arrow.
- BUFFER (419)**: Connected to **CPU 301** via a bidirectional arrow.
- PRINT HEAD (420)**: Connected to **CPU 301** via a bidirectional arrow.

CPU 301

FIG. 6

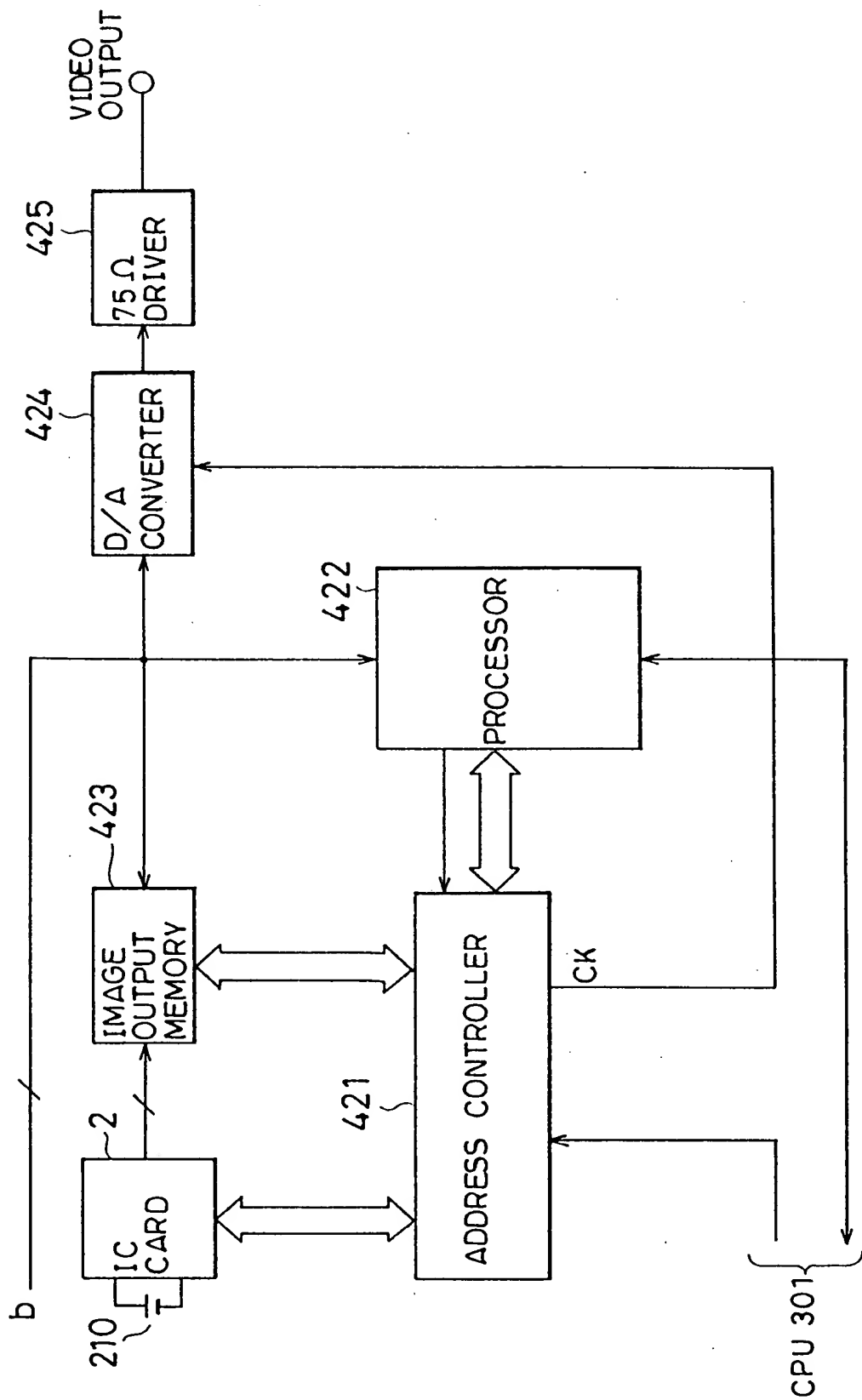


FIG.7

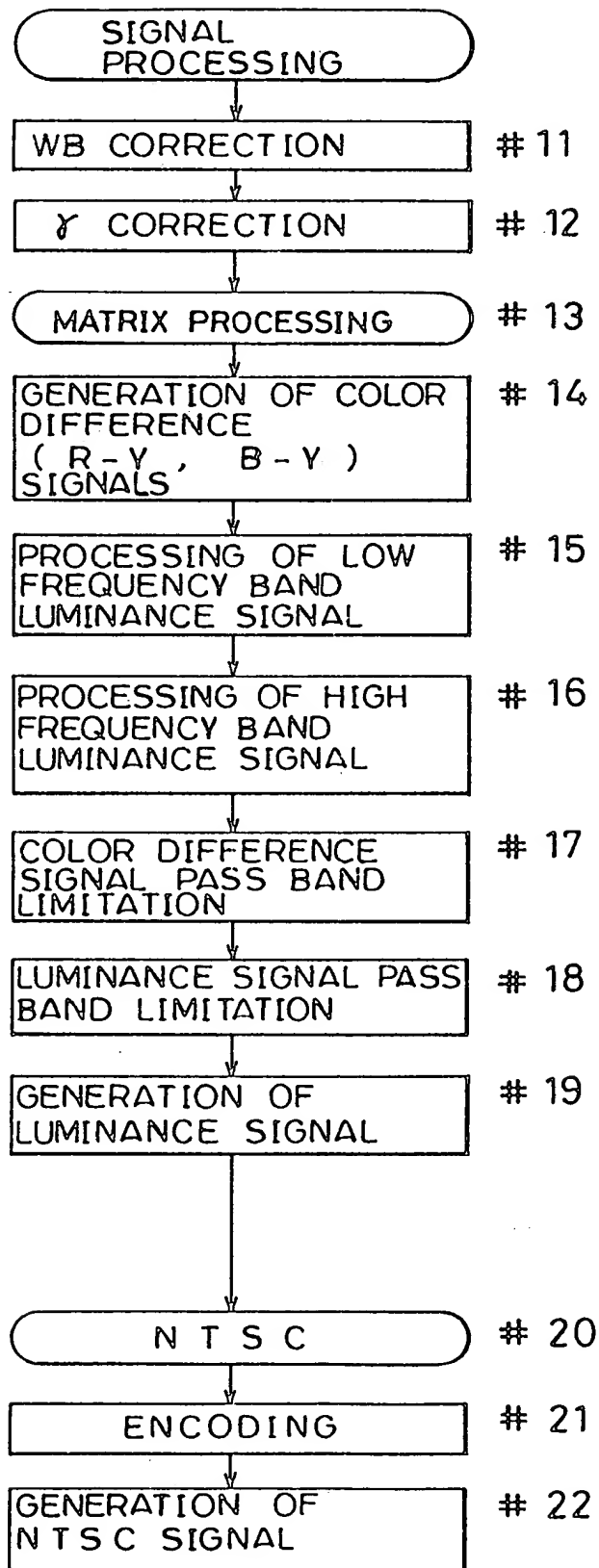


FIG. 8

756 HORIZONTAL PICTURE
ELEMENTS

VERTICAL ROWS
(LINES)

| | | | | | | | | | | |
|---|---|---|---|---|--|---|---|---|---|---|
| R | G | B | R | G | | G | B | R | G | B |
| R | G | B | R | G | | G | B | R | G | B |
| R | G | B | R | G | | G | B | R | G | B |
| | | | | | | | | | | |
| R | G | B | R | G | | G | B | R | G | B |
| R | G | B | R | G | | G | B | R | G | B |

FIG. 9A

FIG. 9B

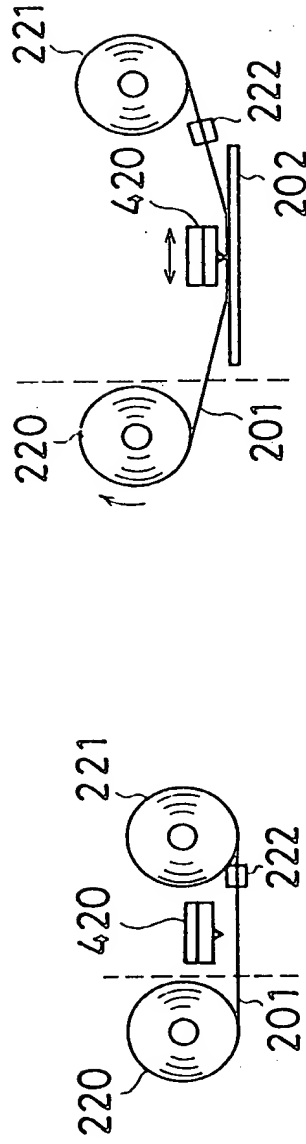


FIG. 10

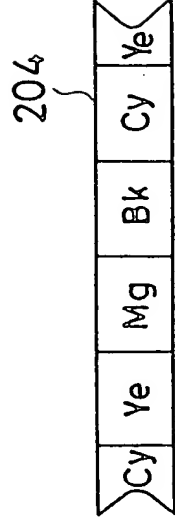


FIG.11

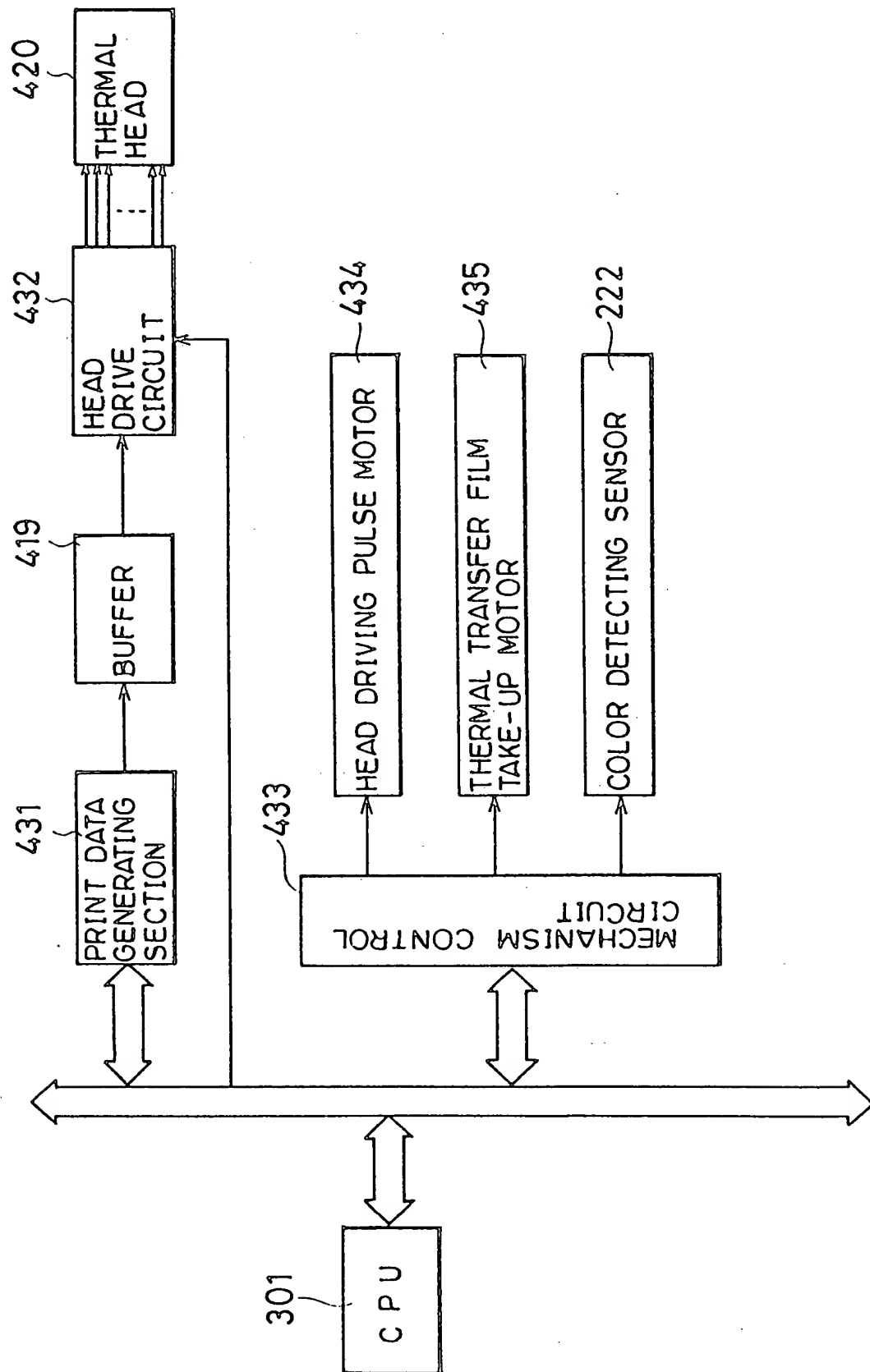


FIG.12

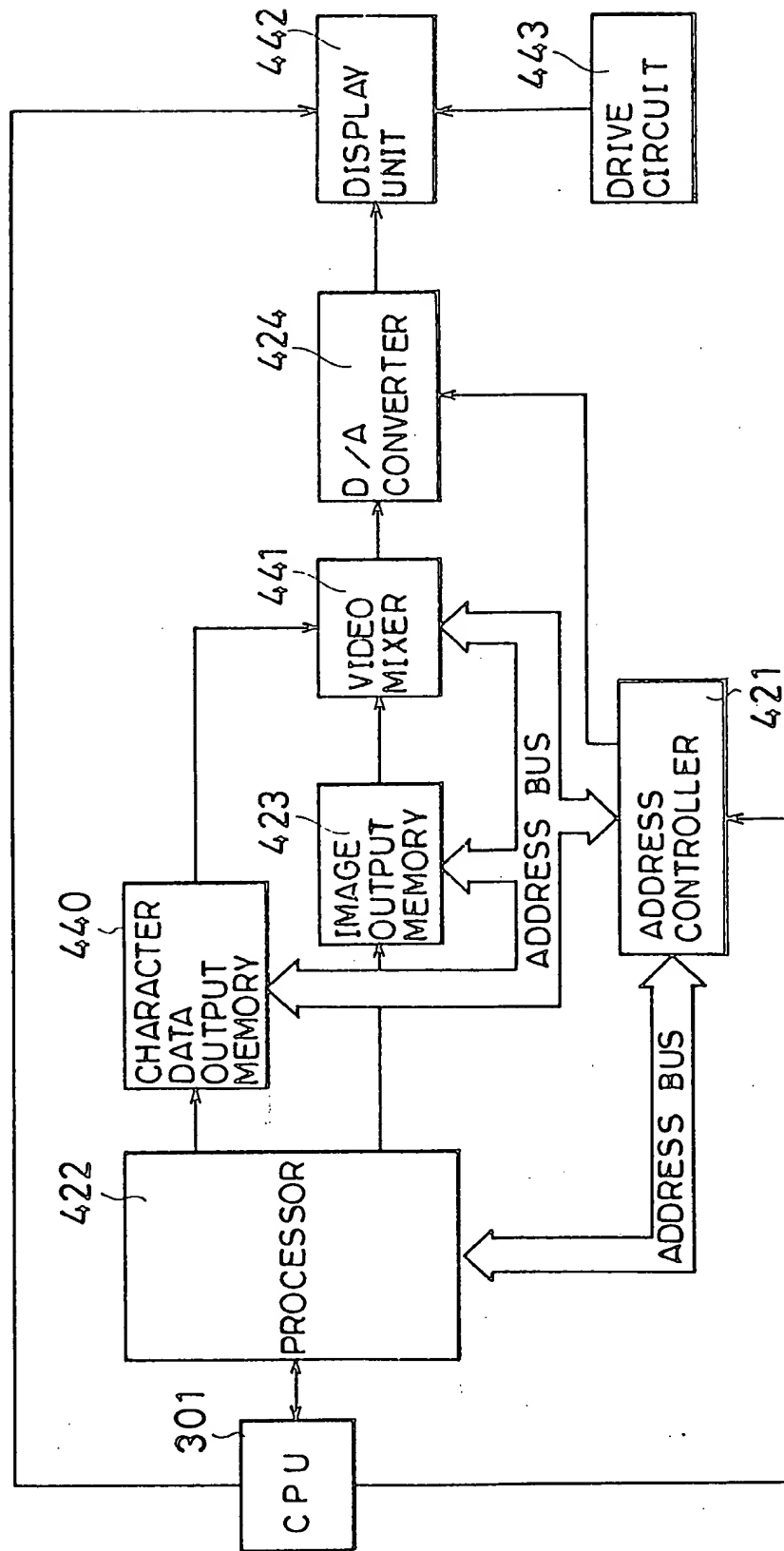


FIG.13A

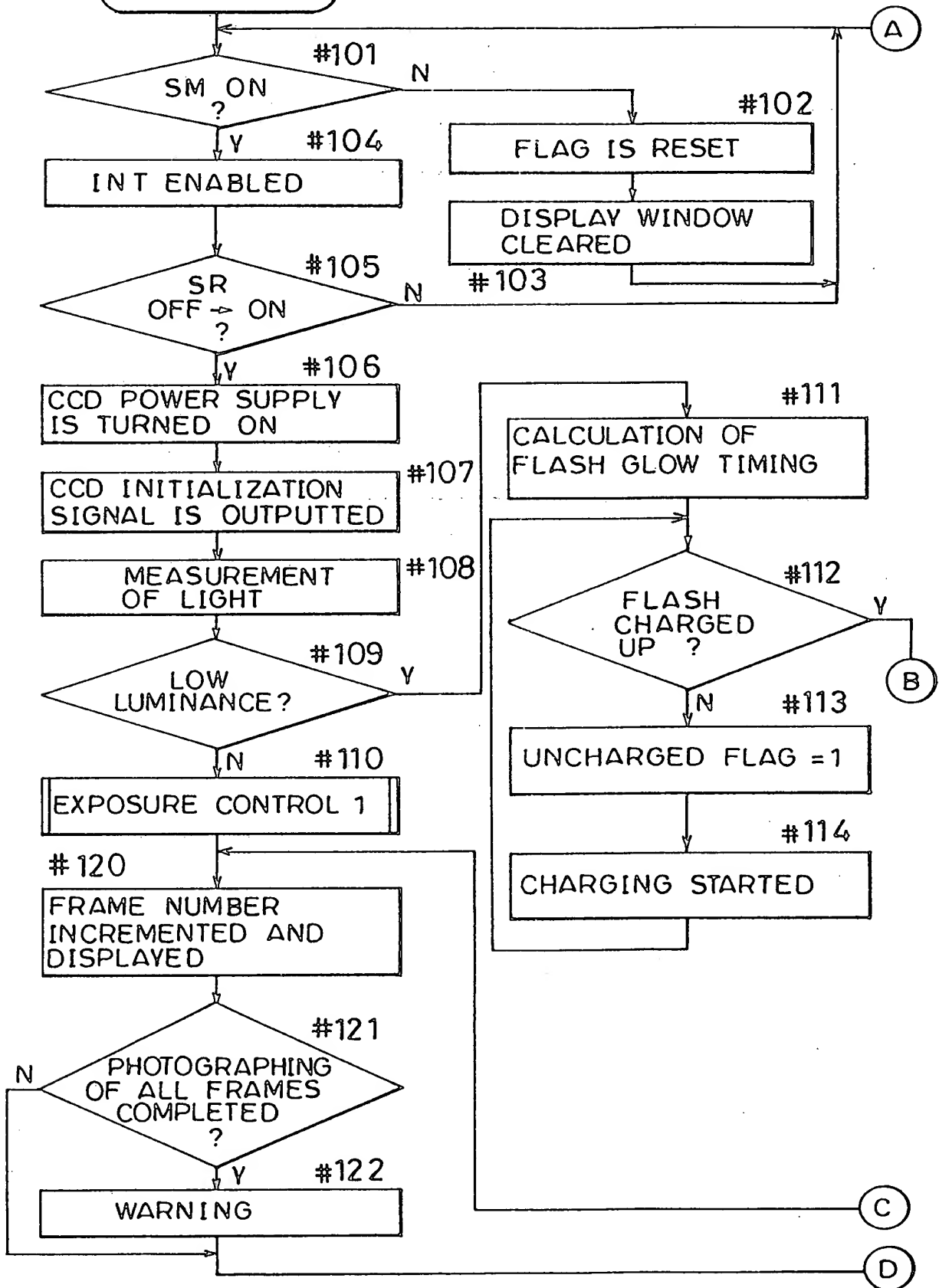


FIG.13B

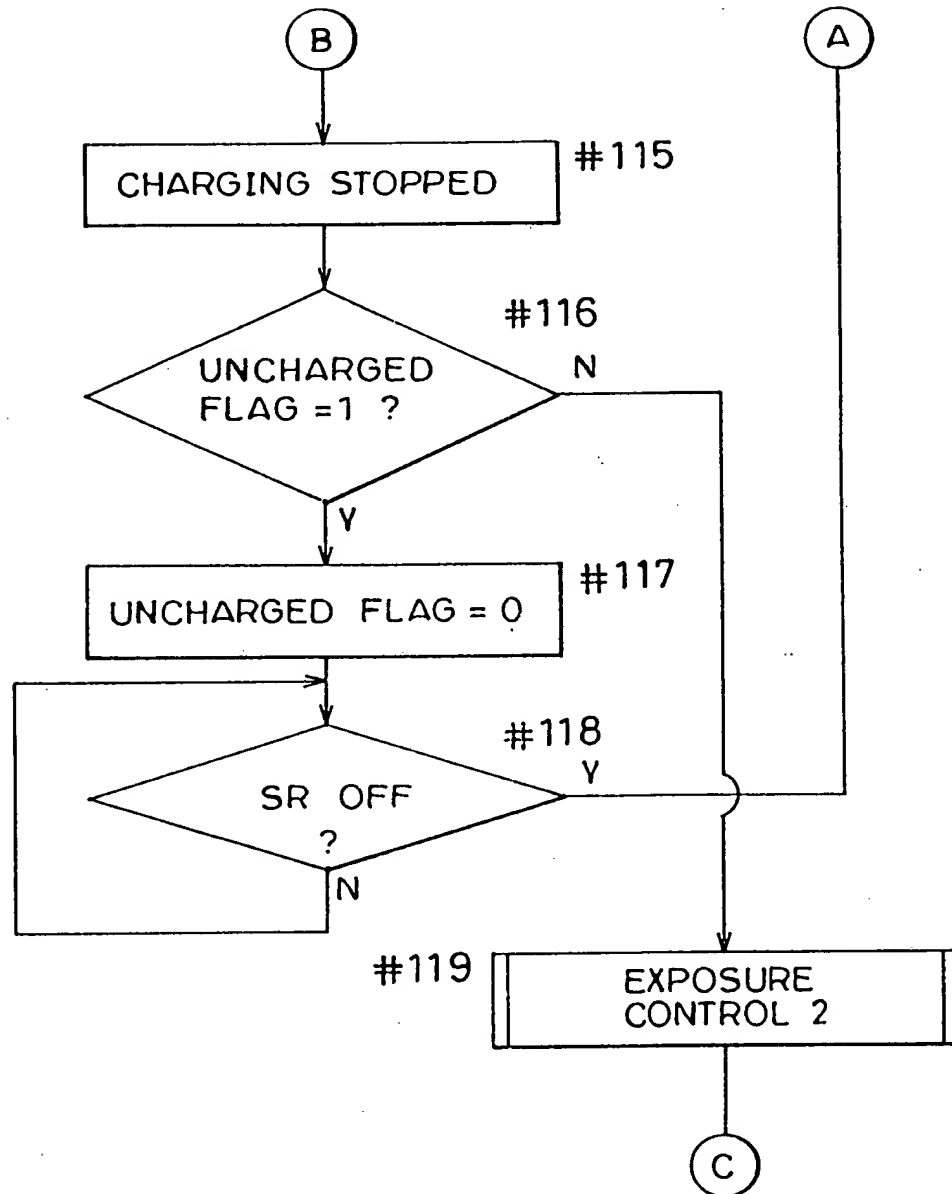


FIG.13C

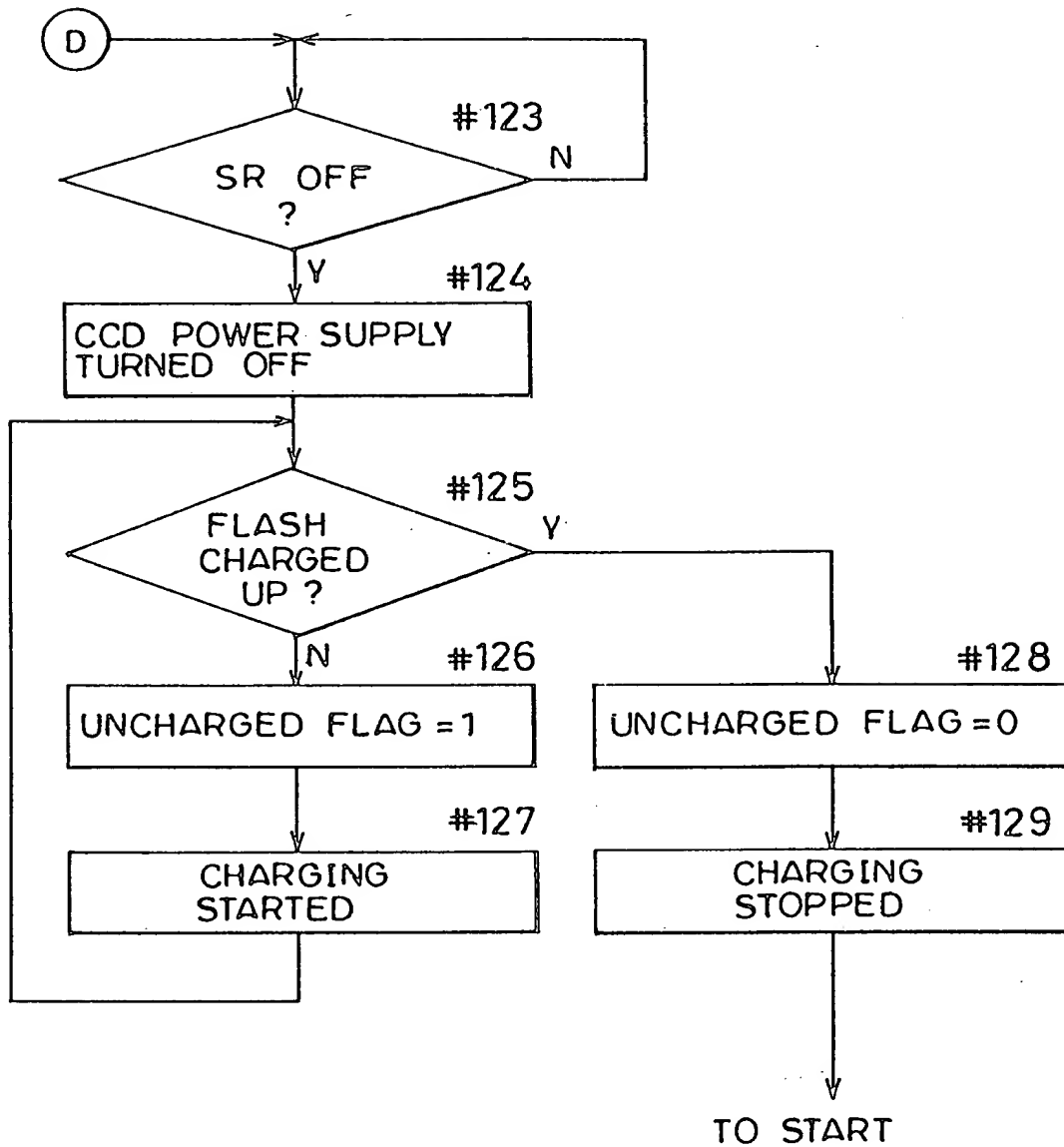


FIG.14A

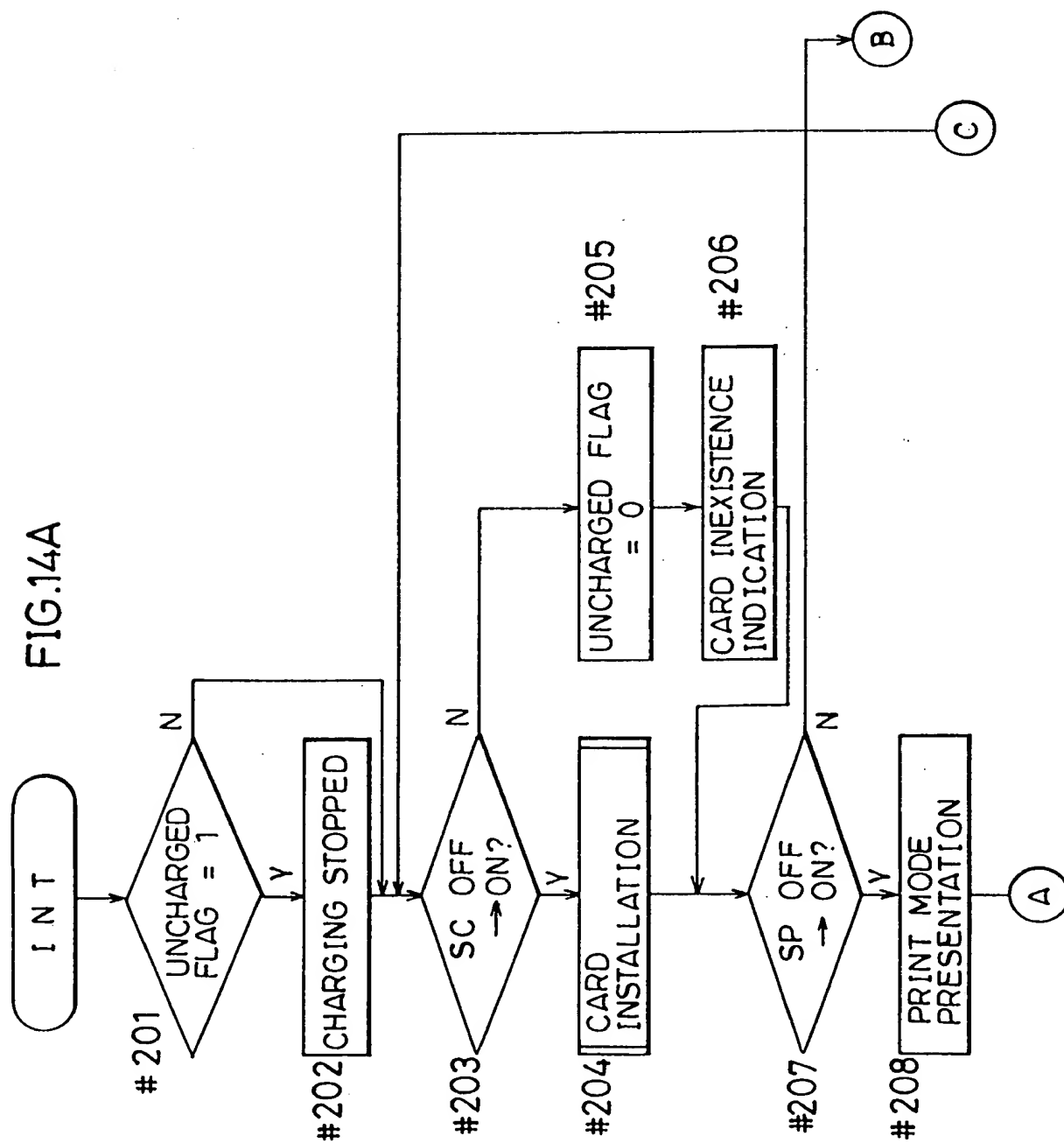


FIG.14B

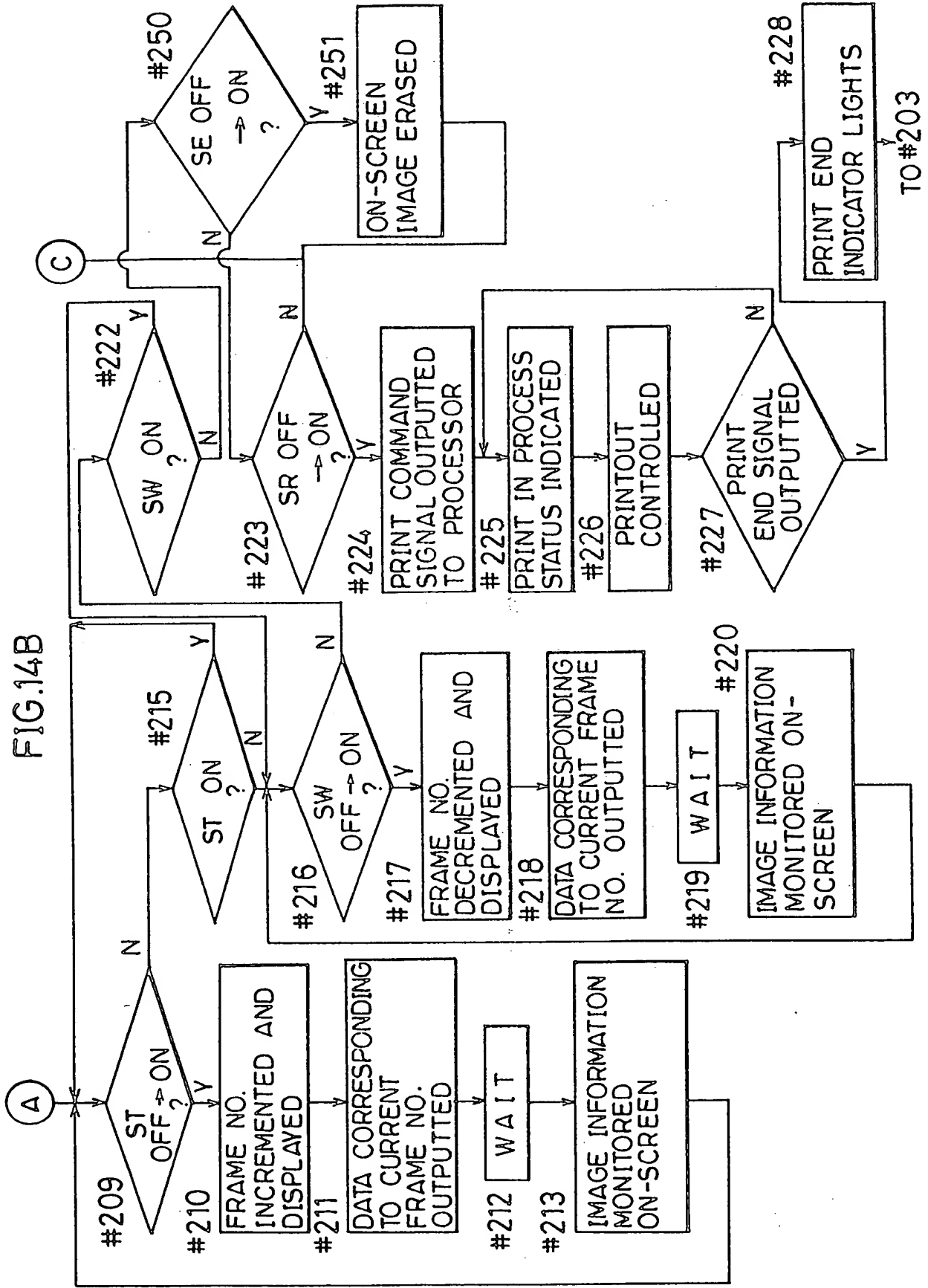


FIG.14C

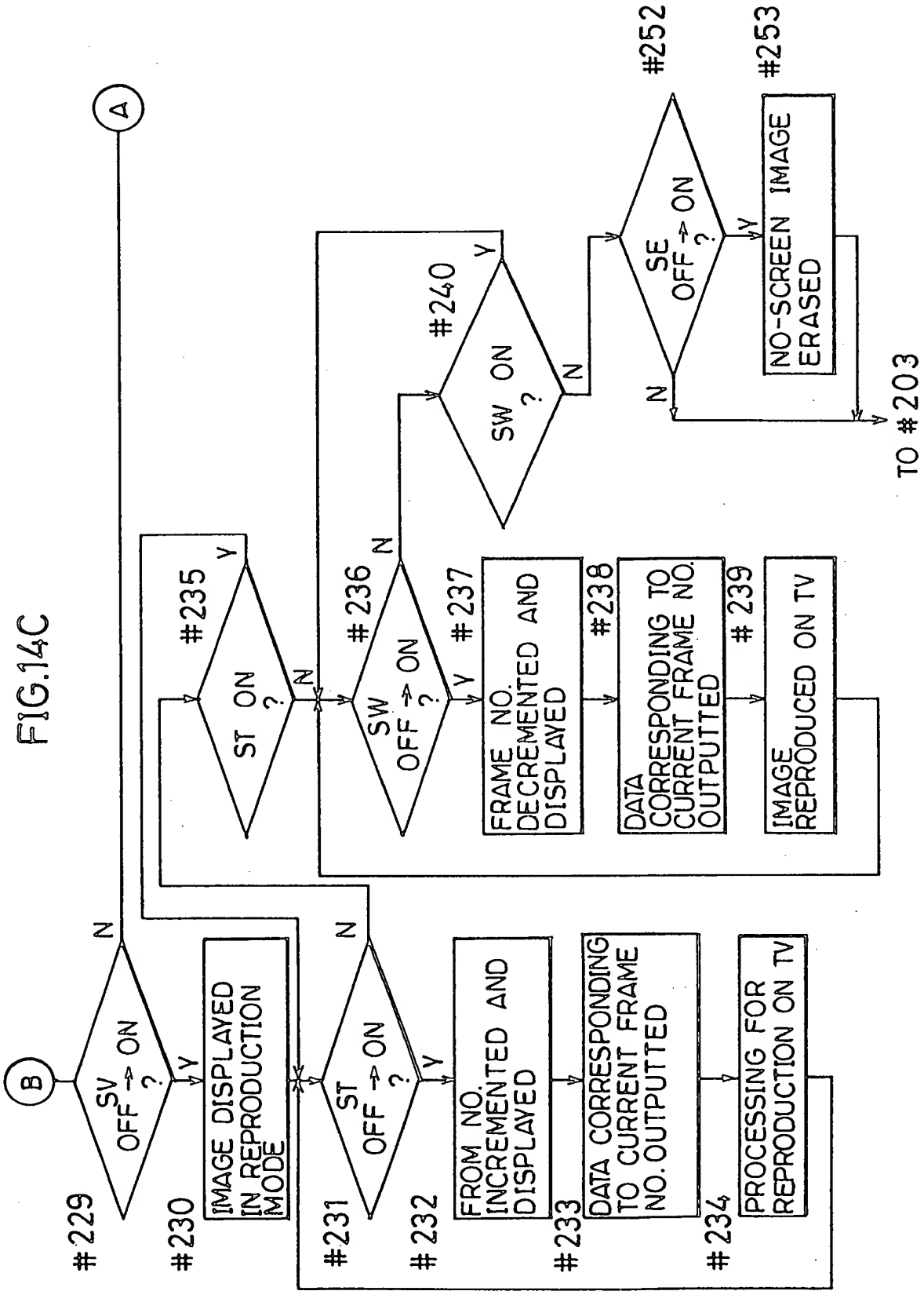


FIG.14D

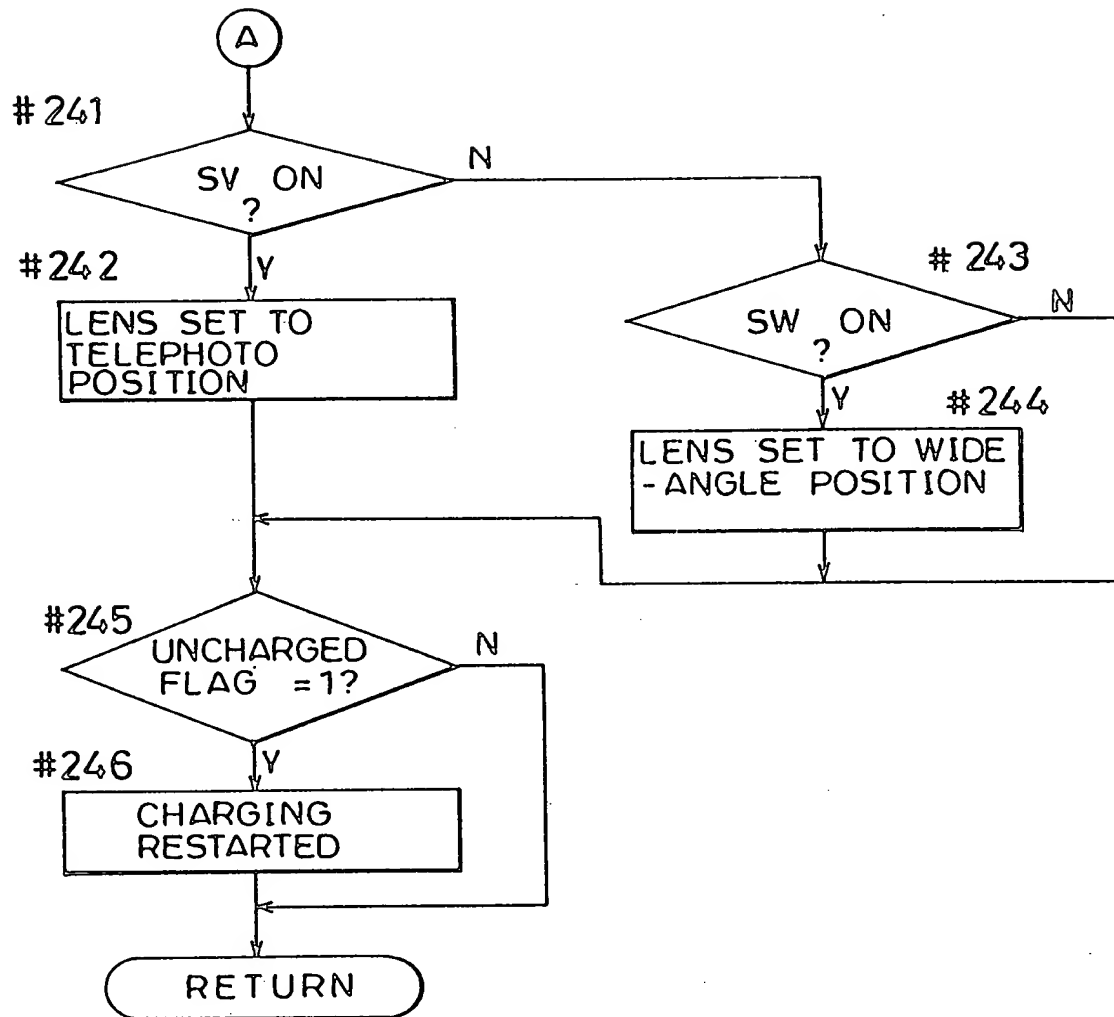


FIG.15A

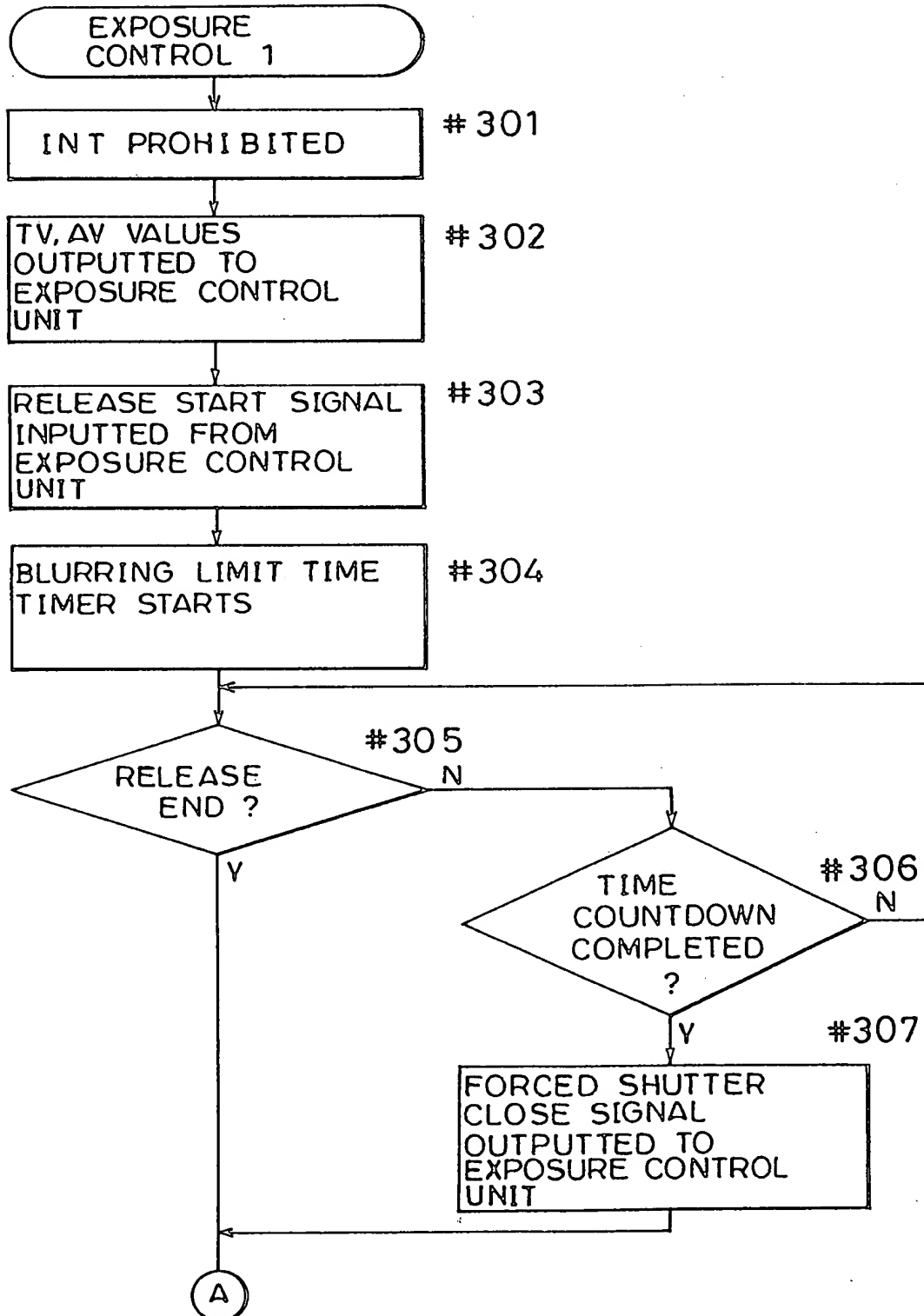


FIG.15B

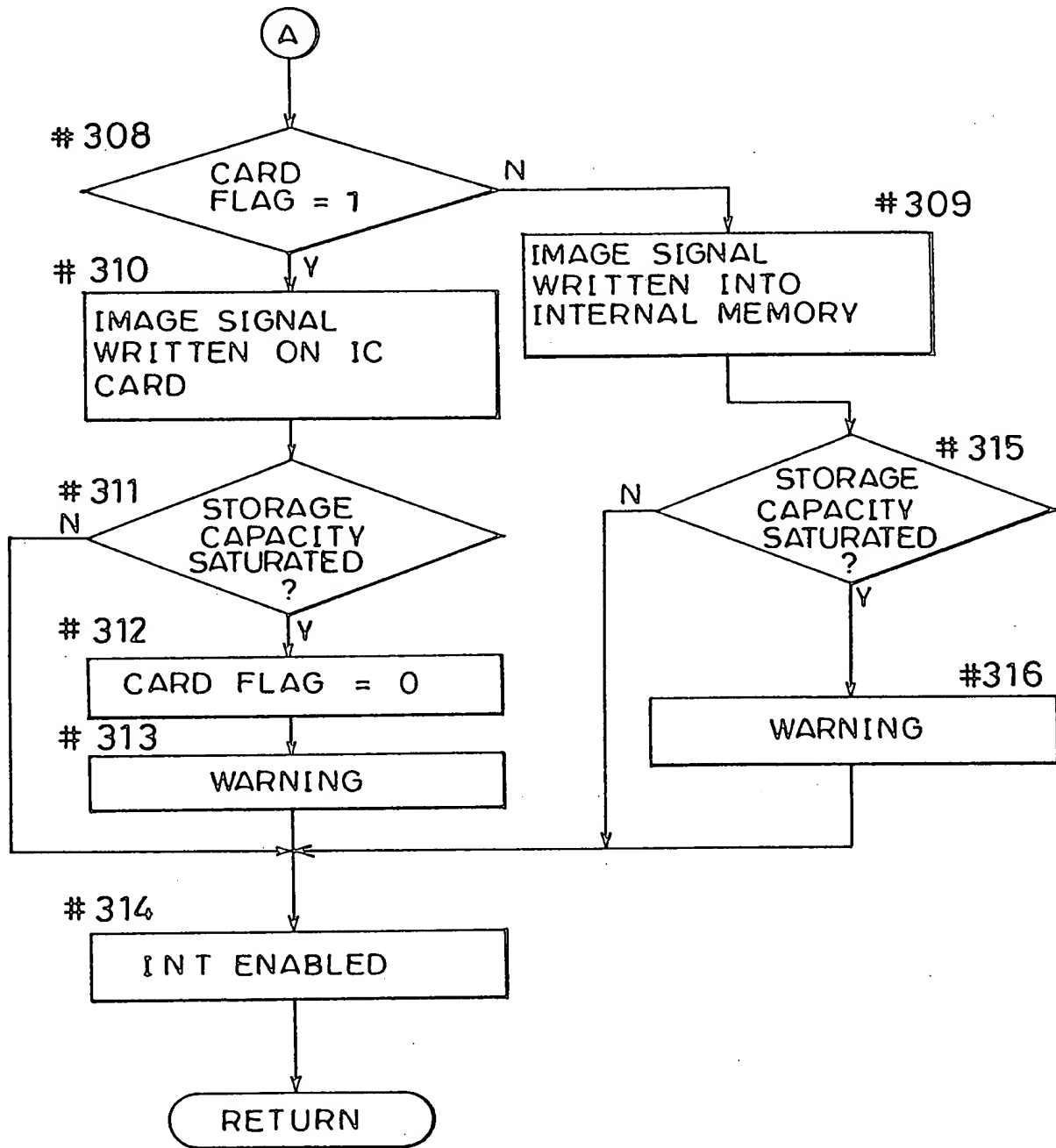


FIG.16A

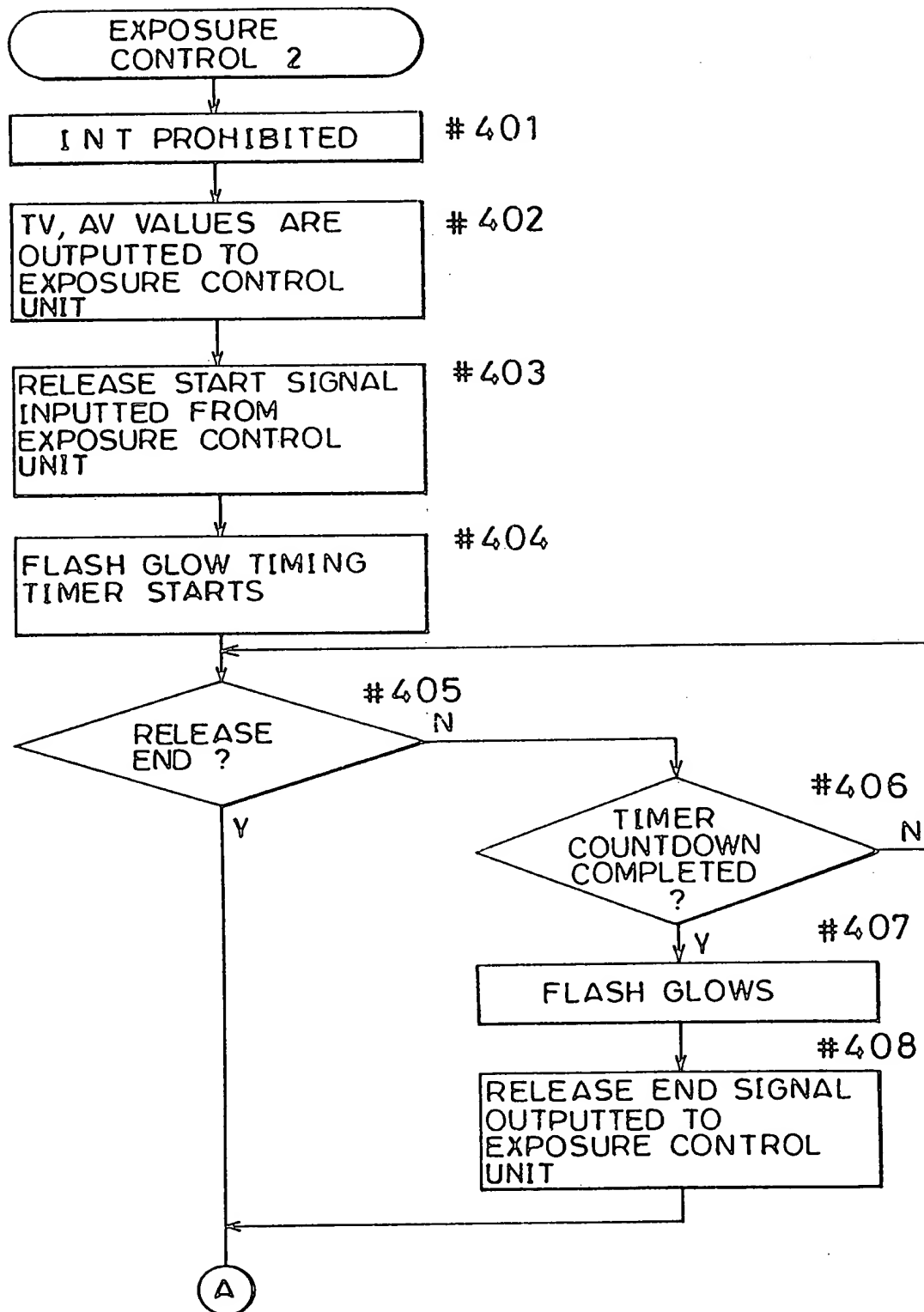


FIG.16B

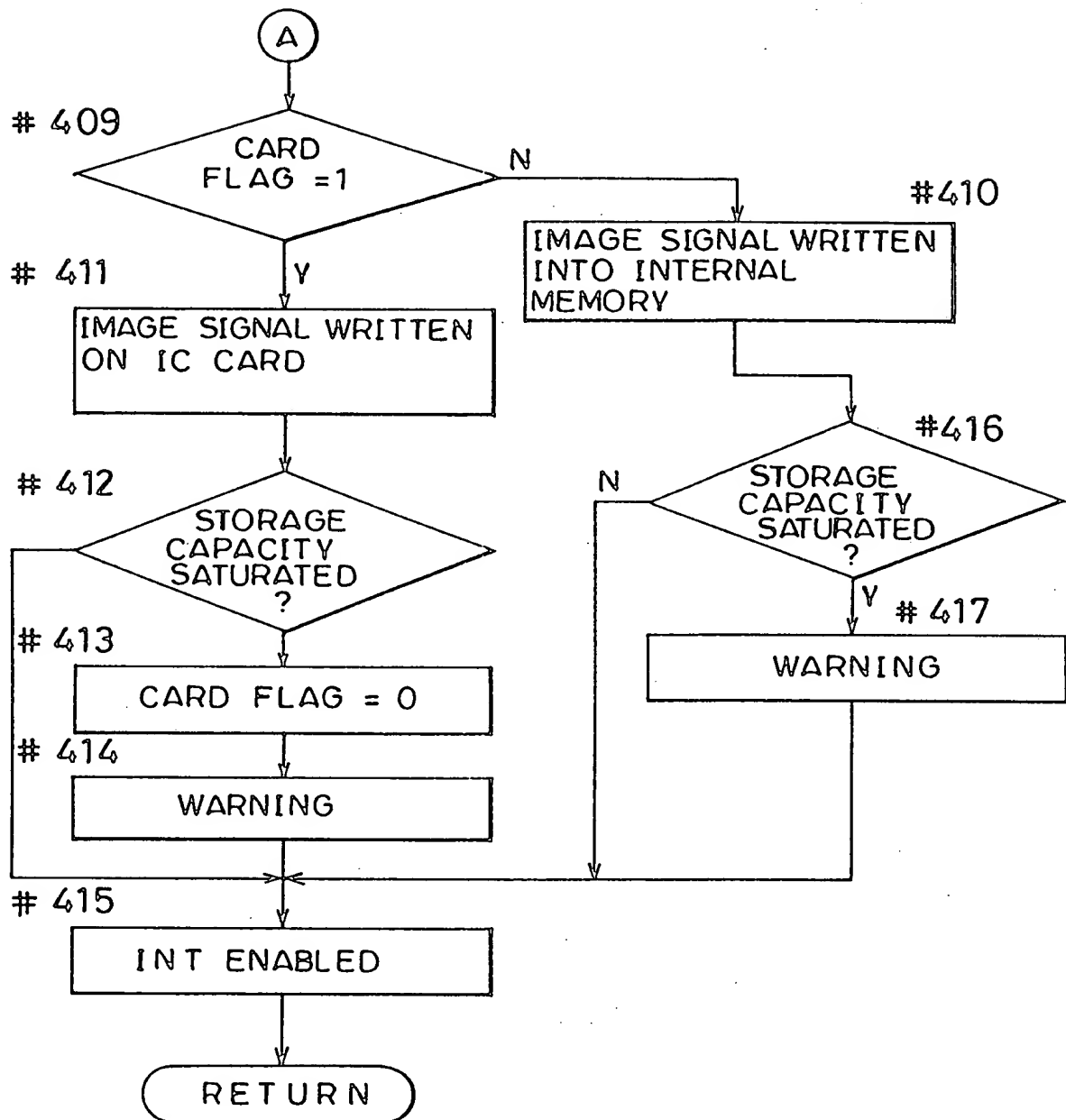


FIG.17

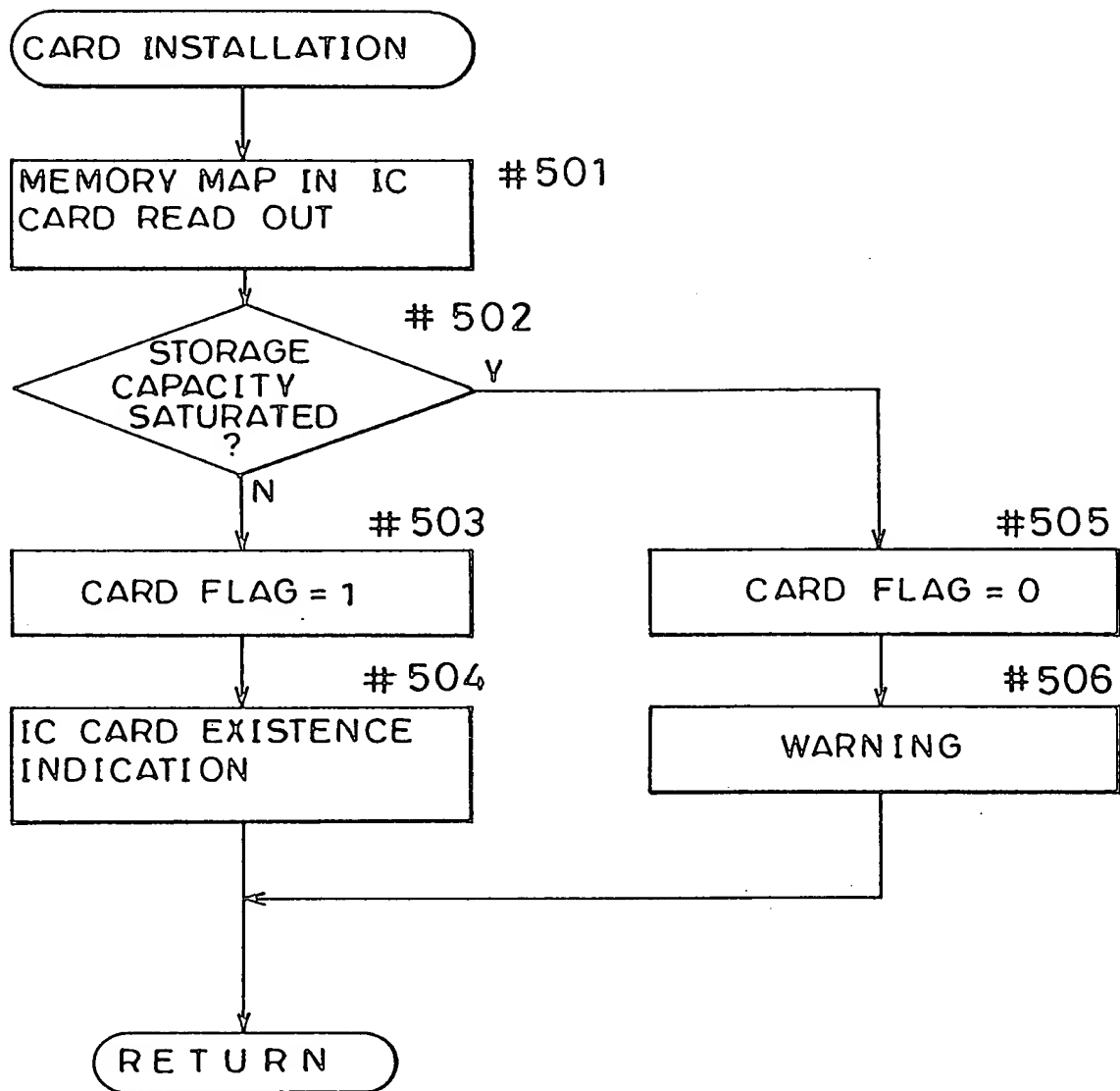


FIG.18

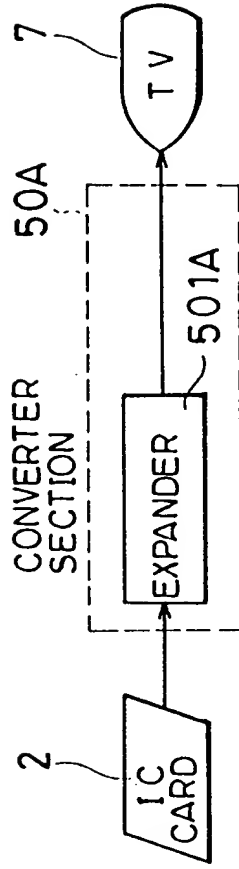


FIG.19

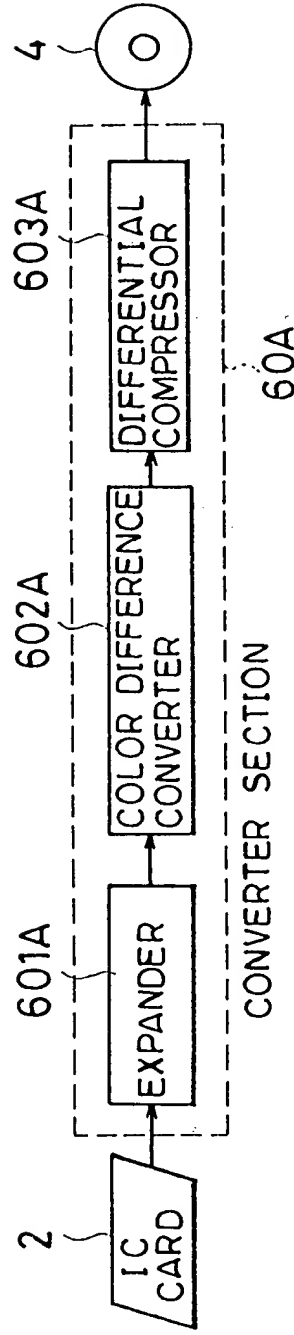


FIG.20

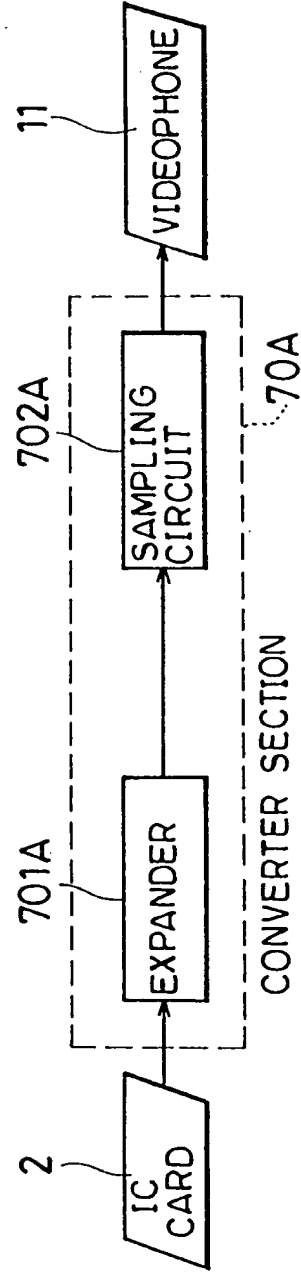
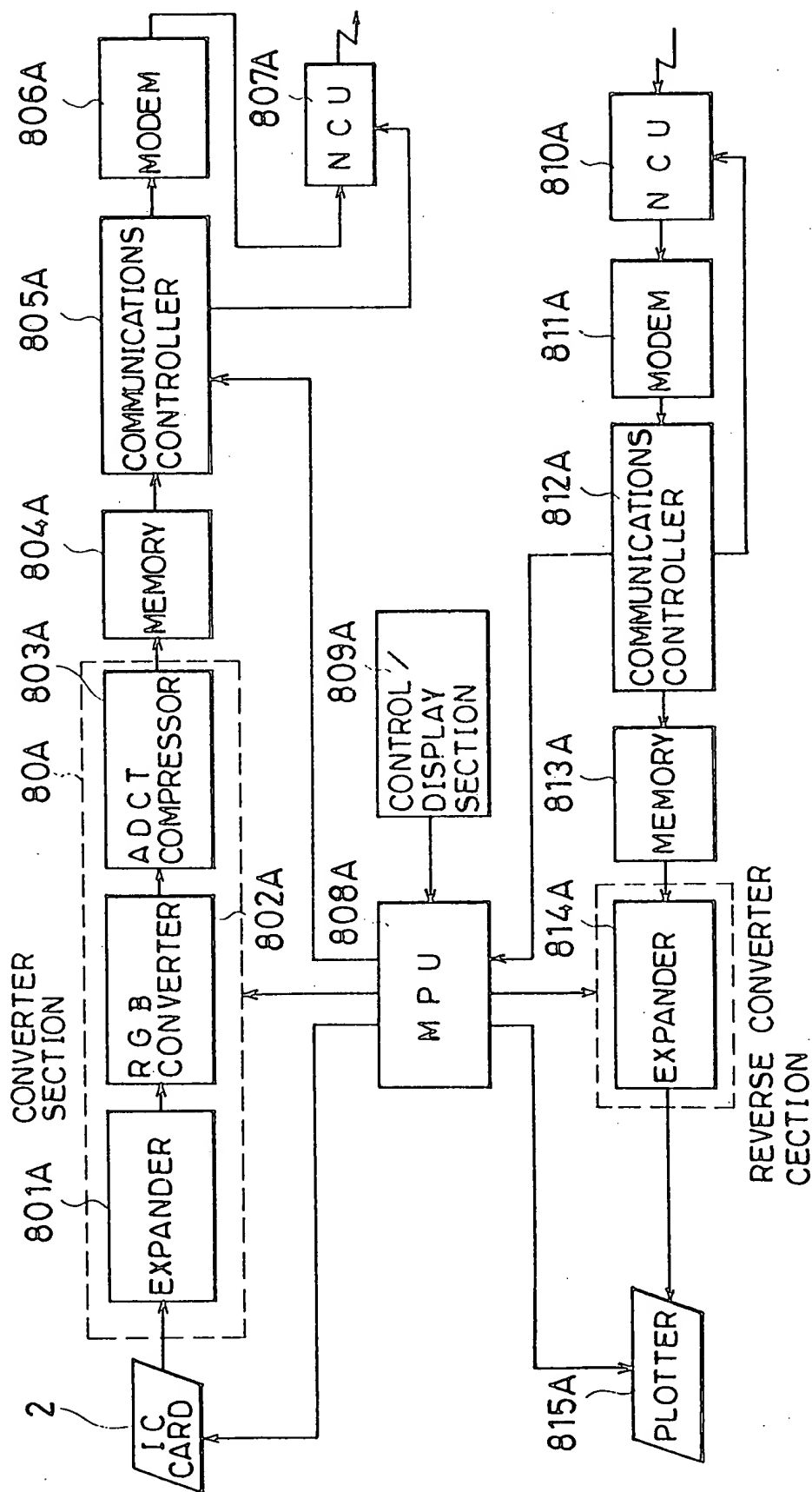


FIG. 21



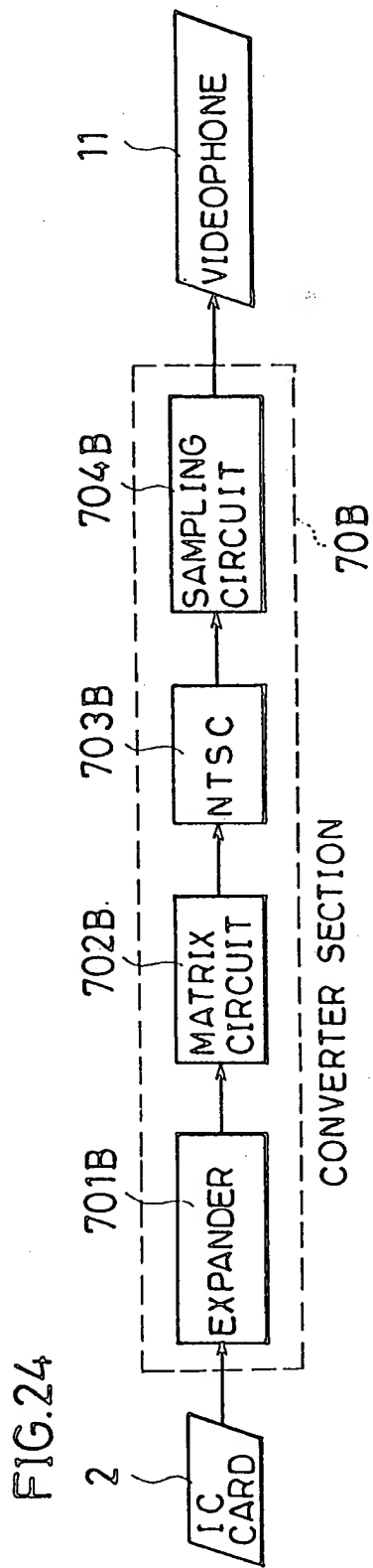
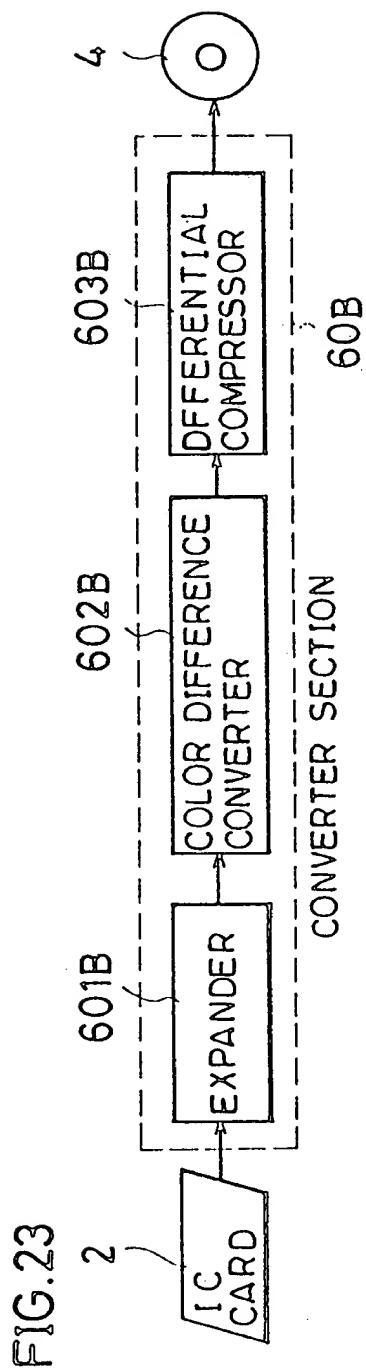
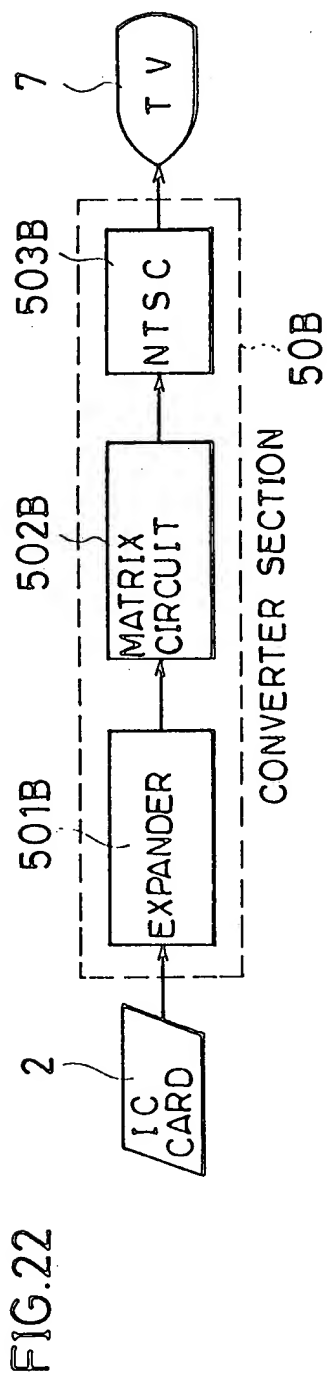


FIG. 25

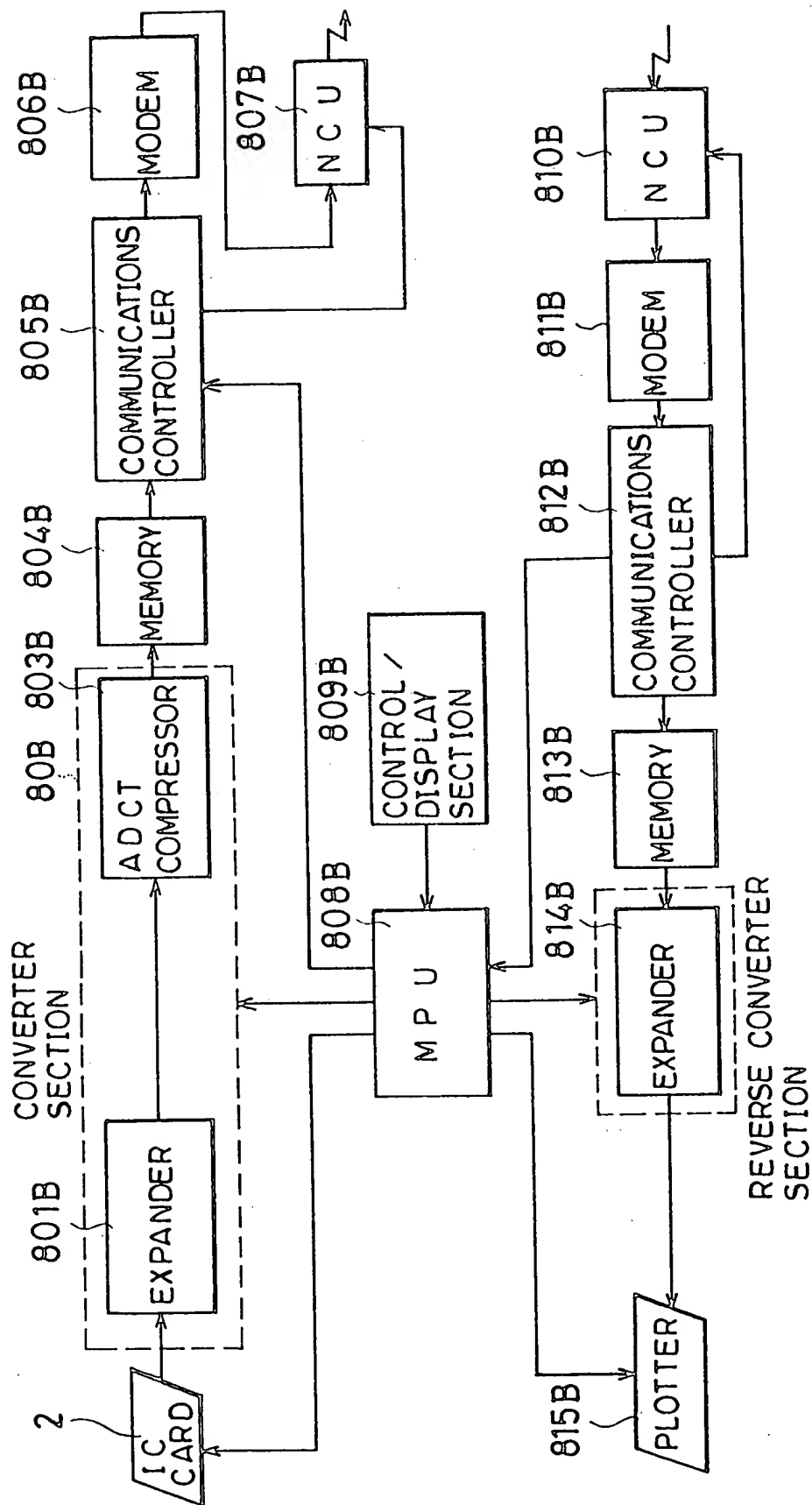


FIG. 26

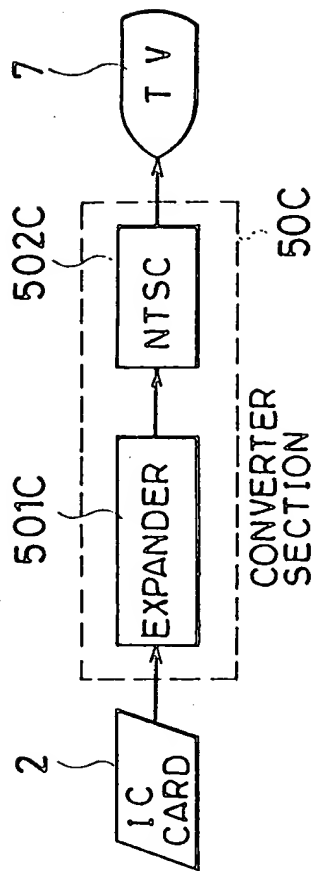


FIG. 27

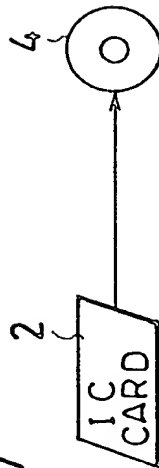


FIG. 28

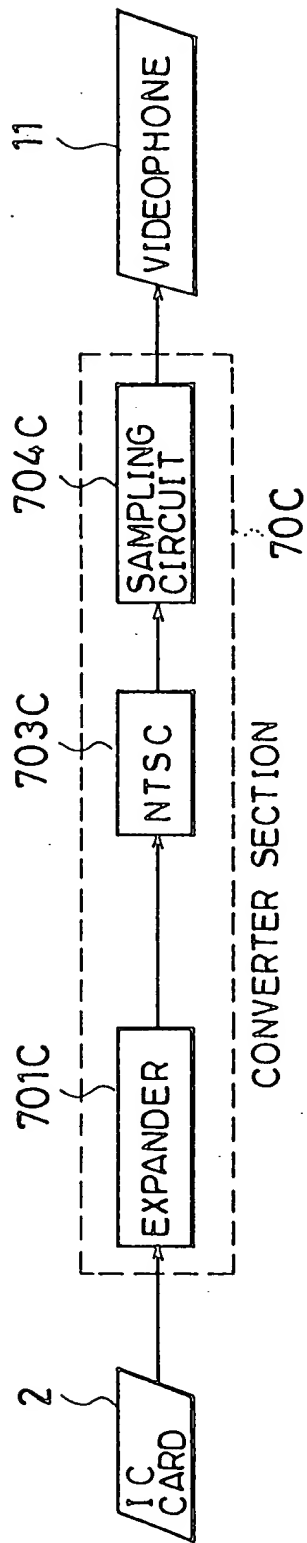


FIG. 29

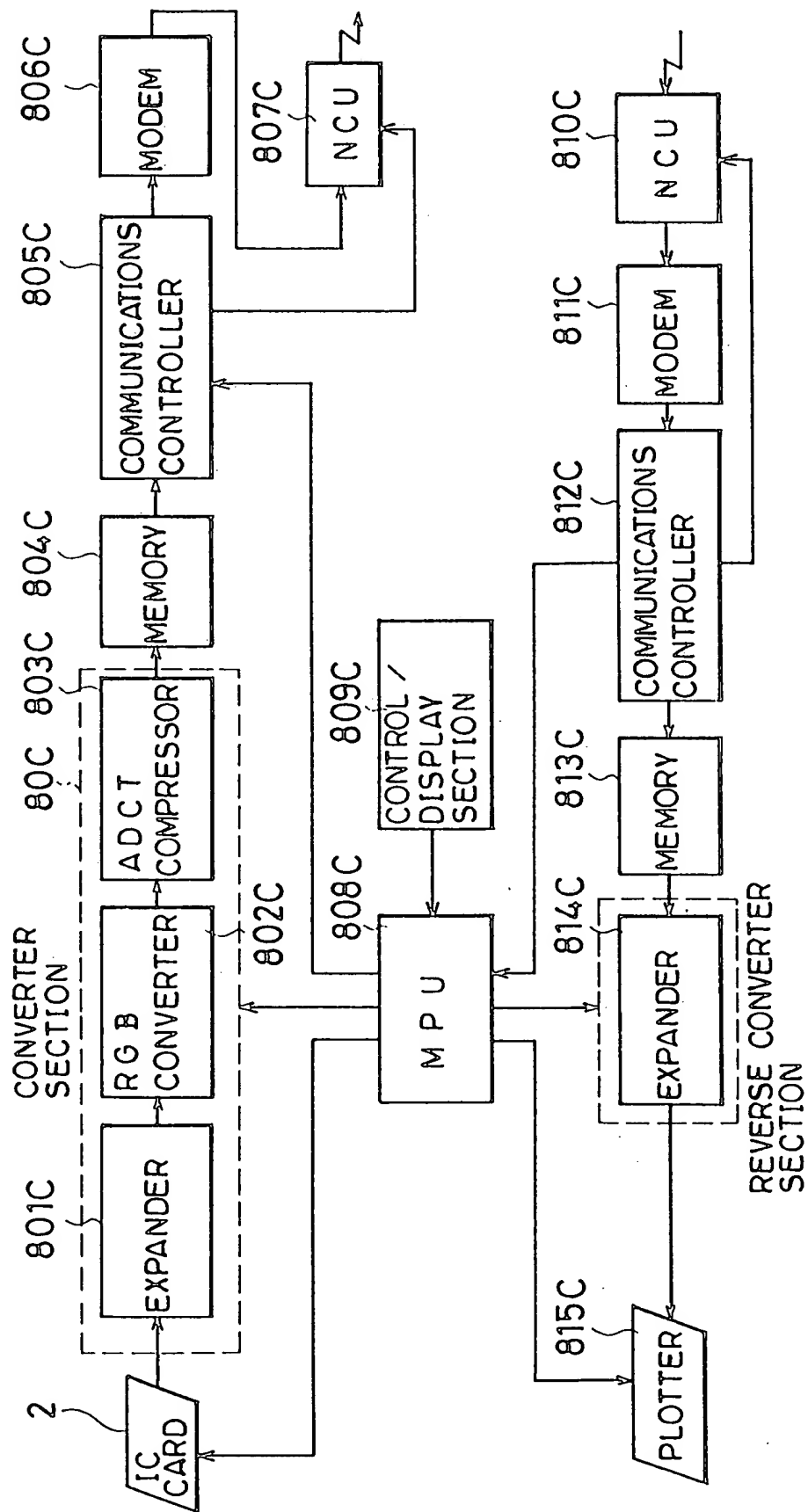


FIG. 30

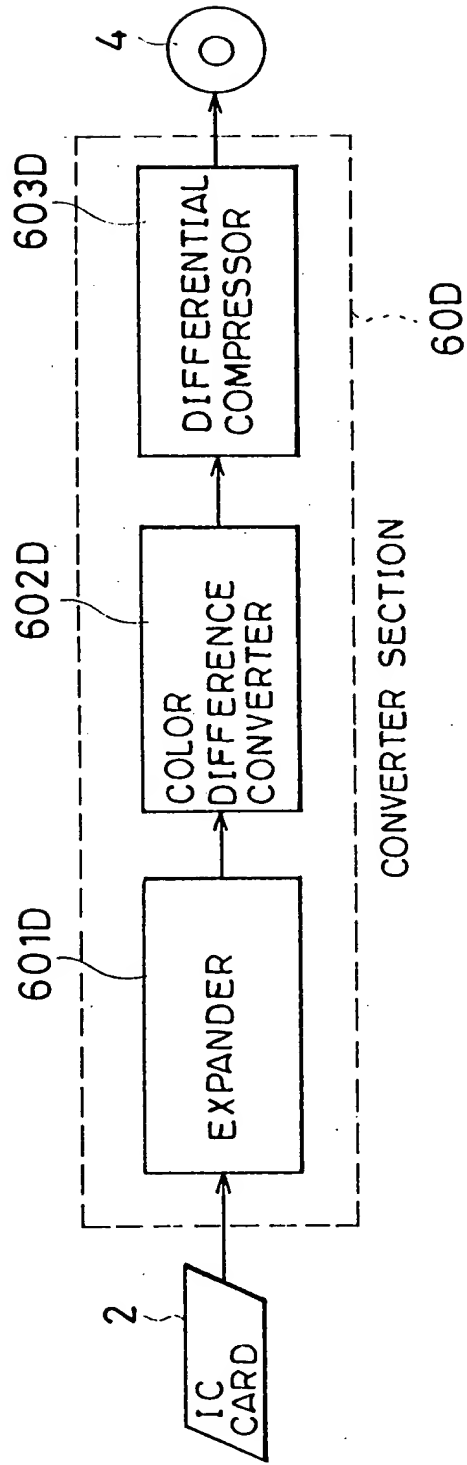


FIG. 31

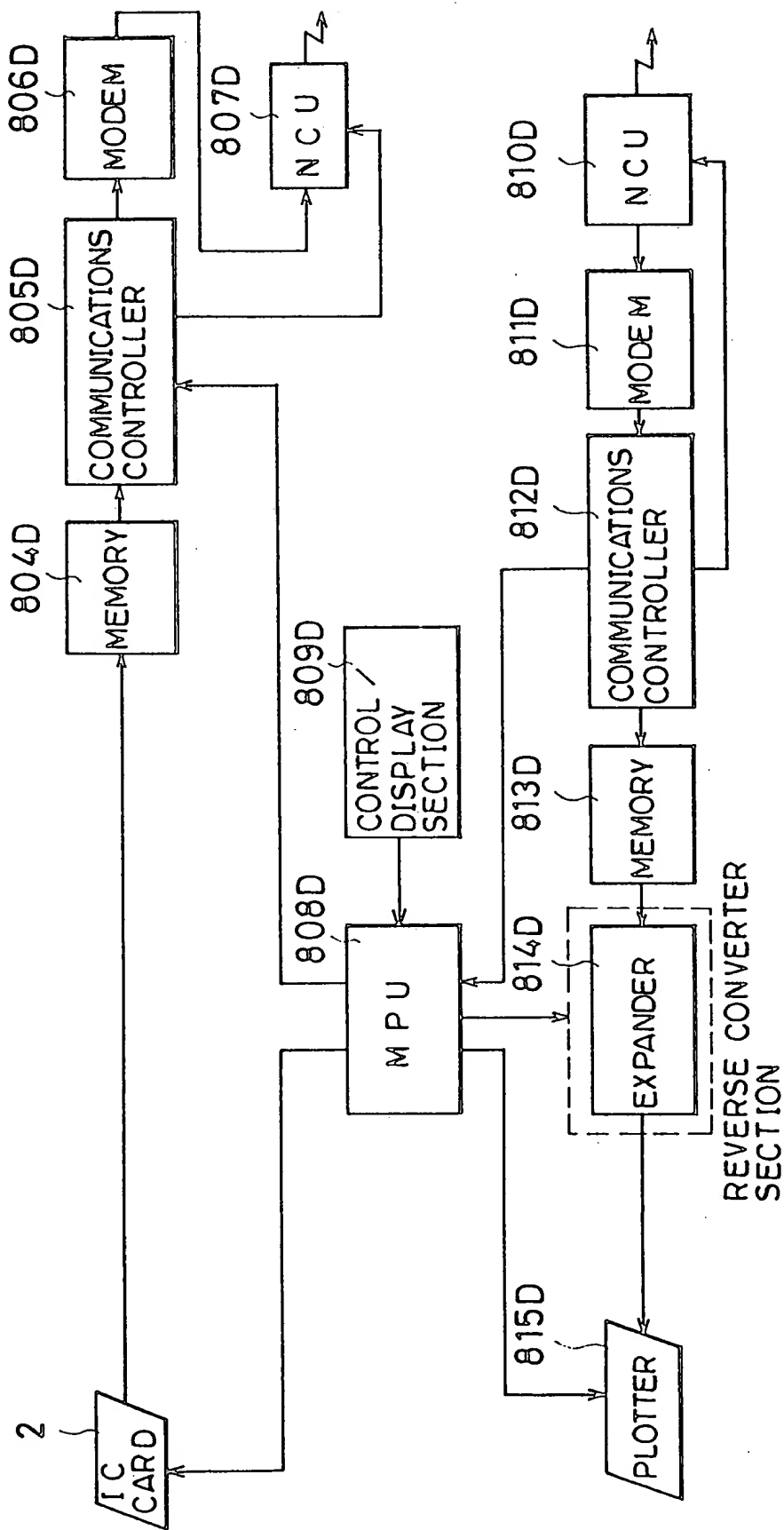


FIG.32

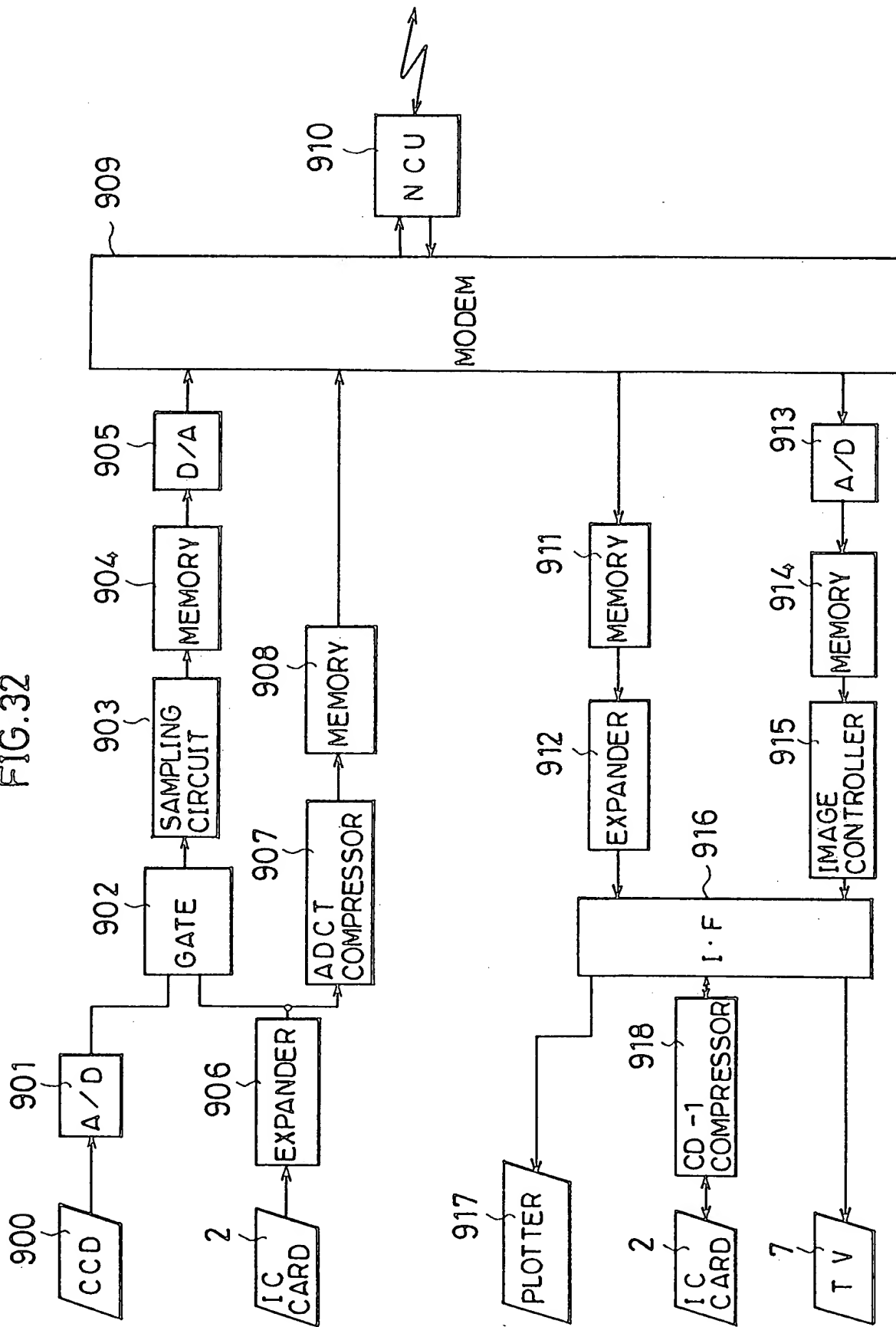


FIG.33

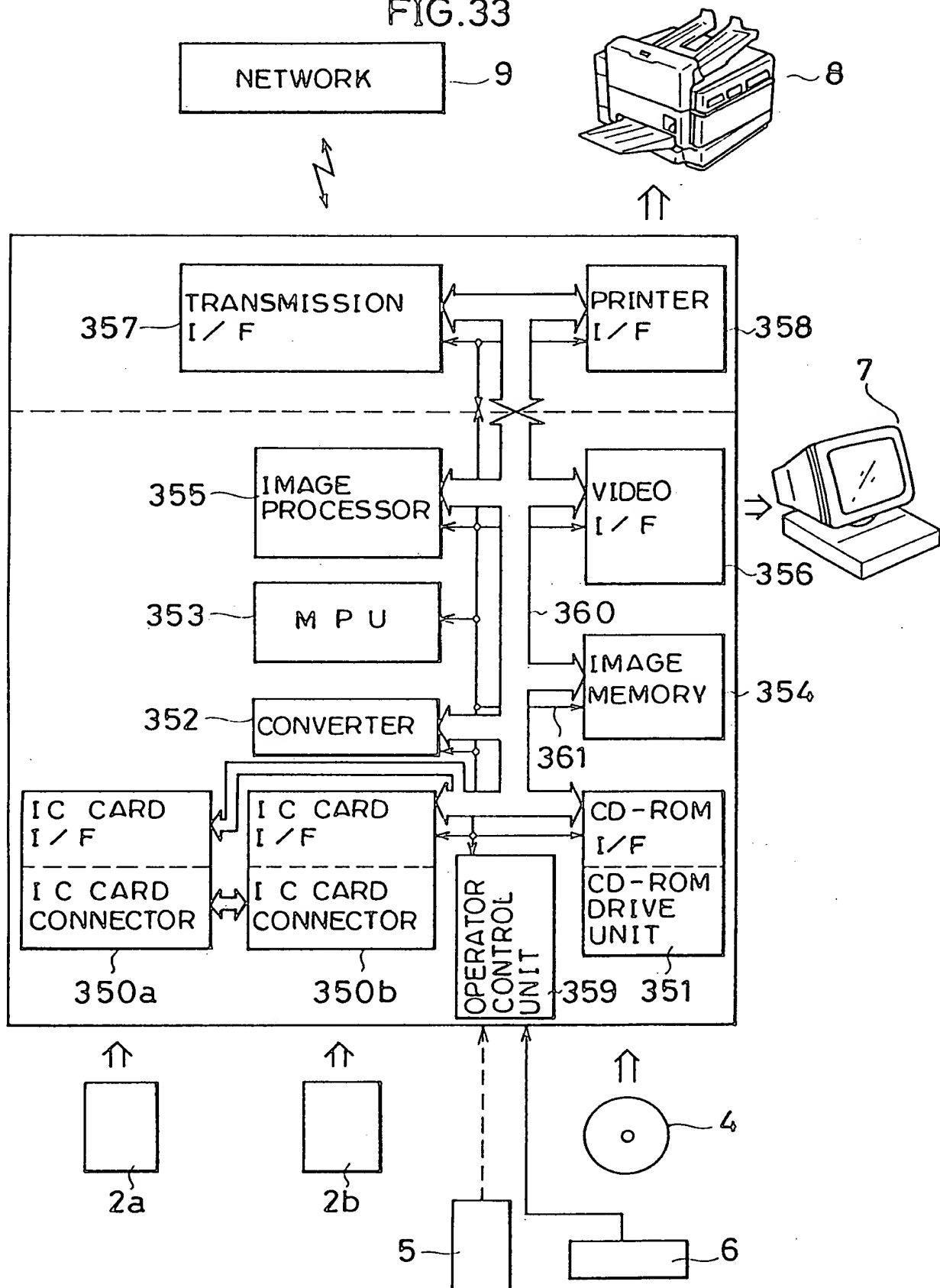


FIG.34

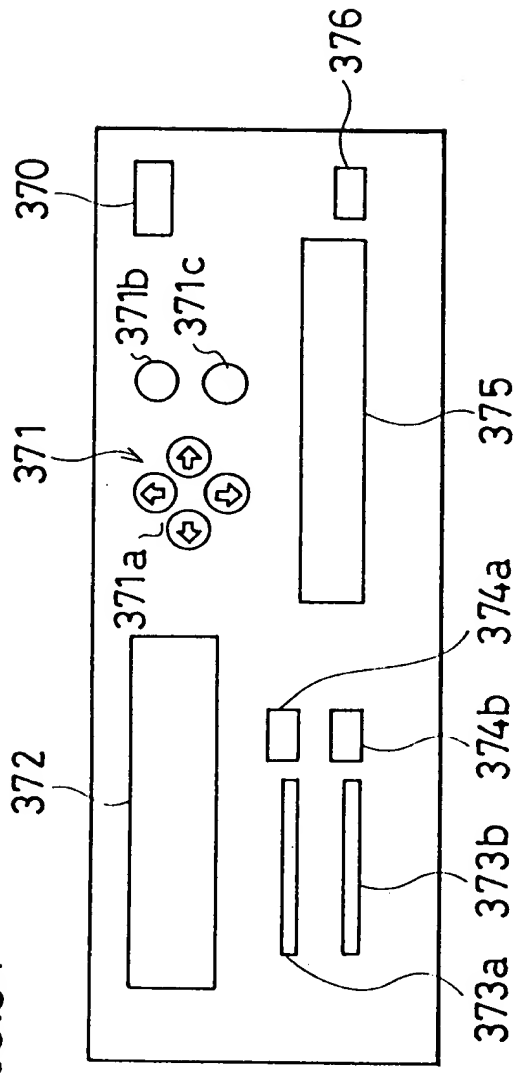


FIG.35

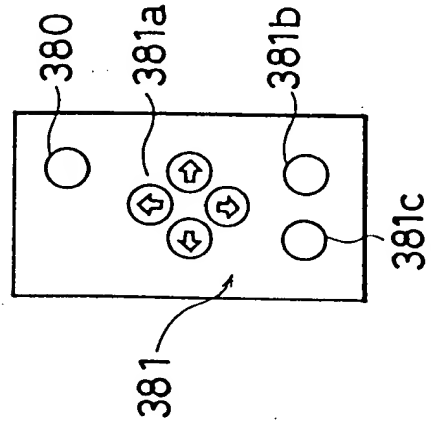


FIG.36

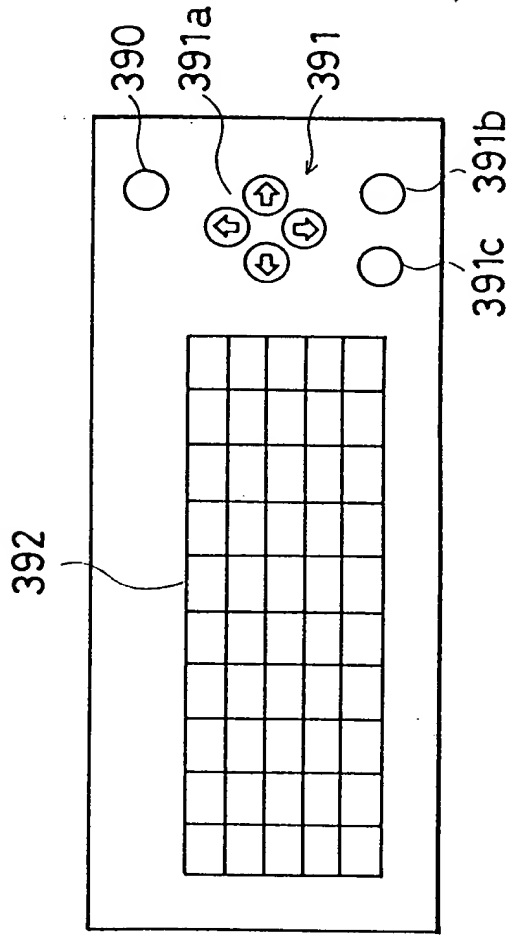


FIG.37

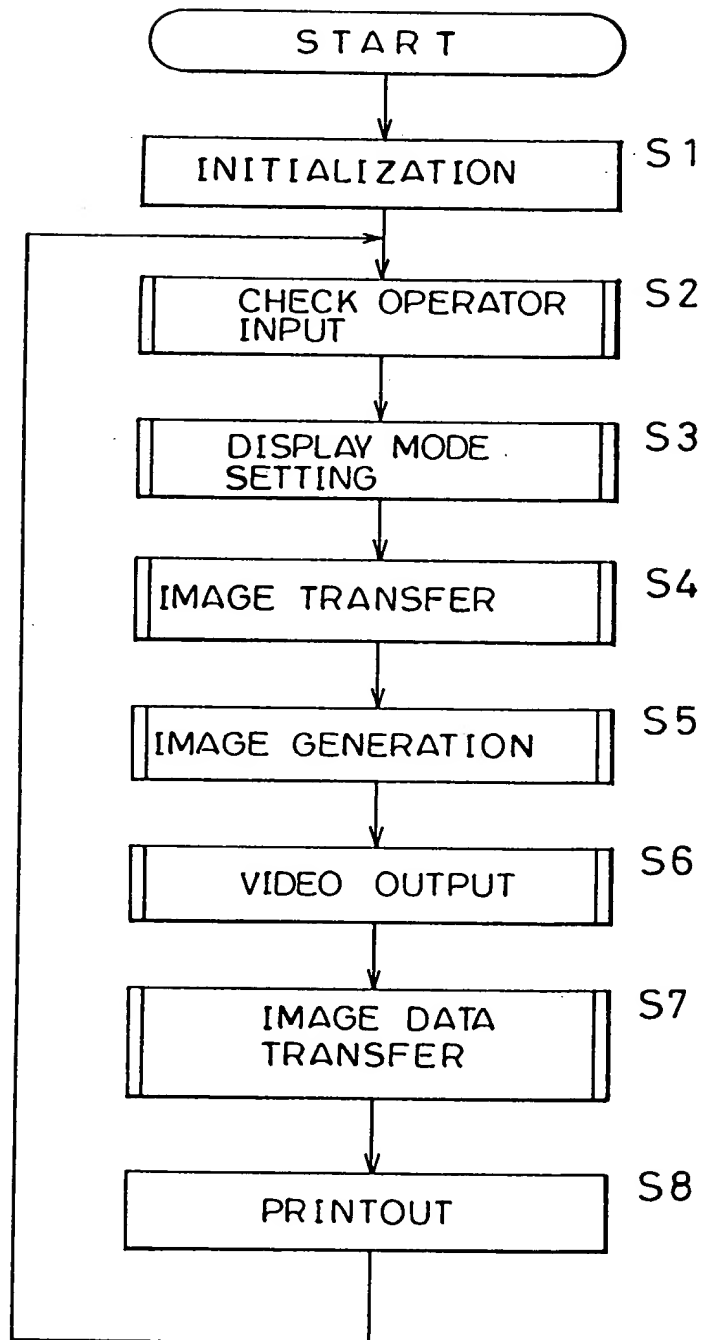


FIG.38

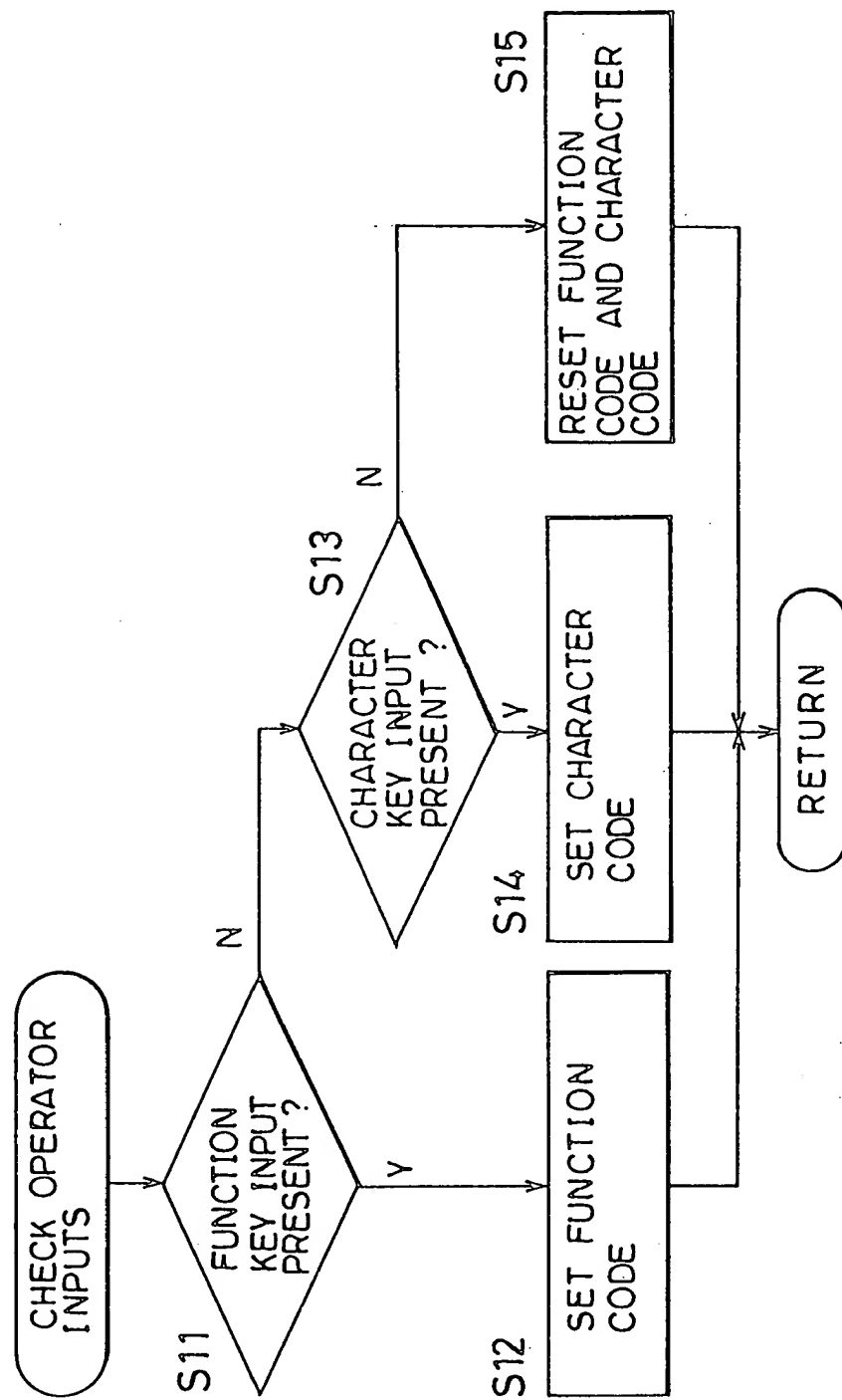


FIG. 39

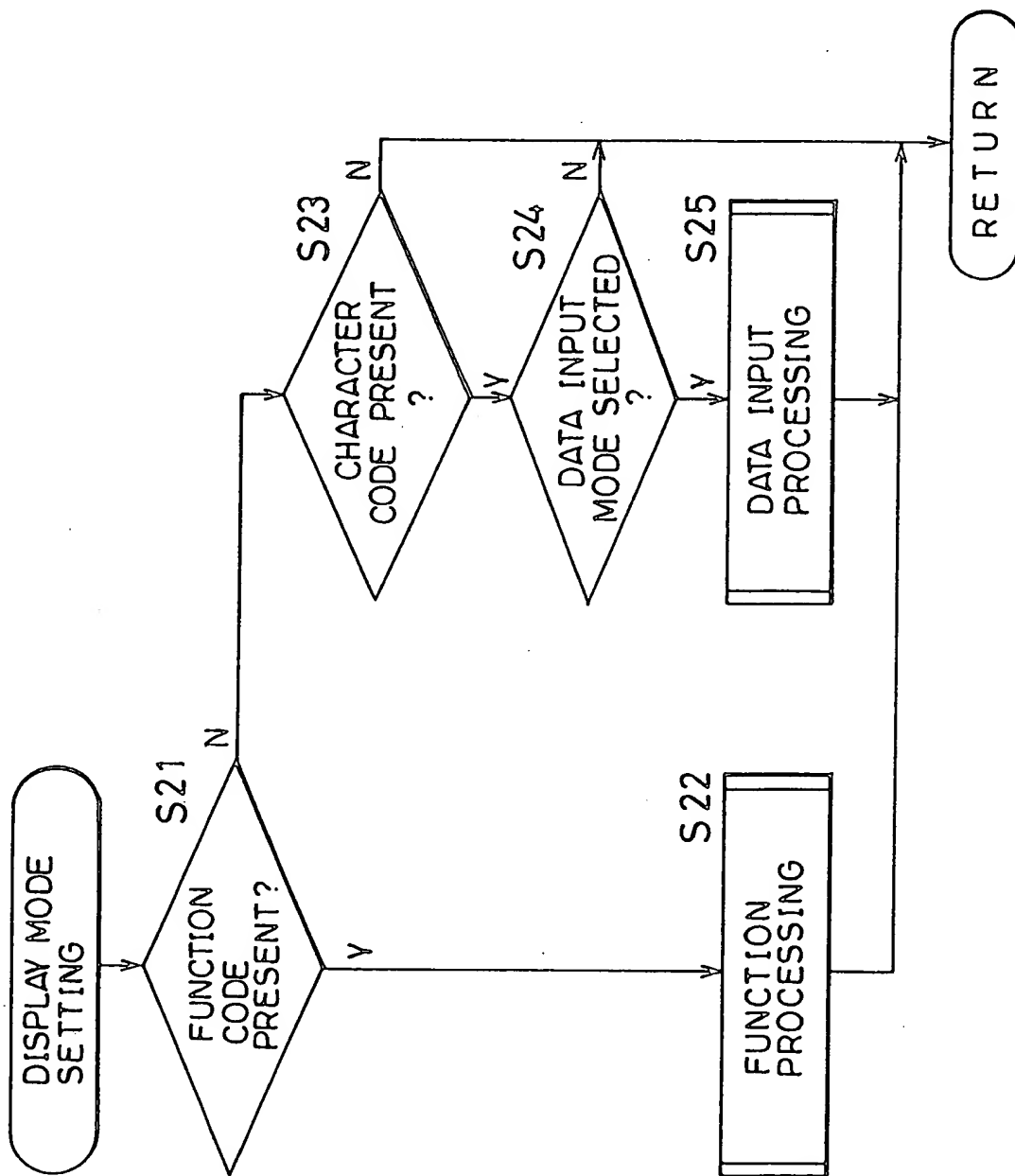


FIG.40

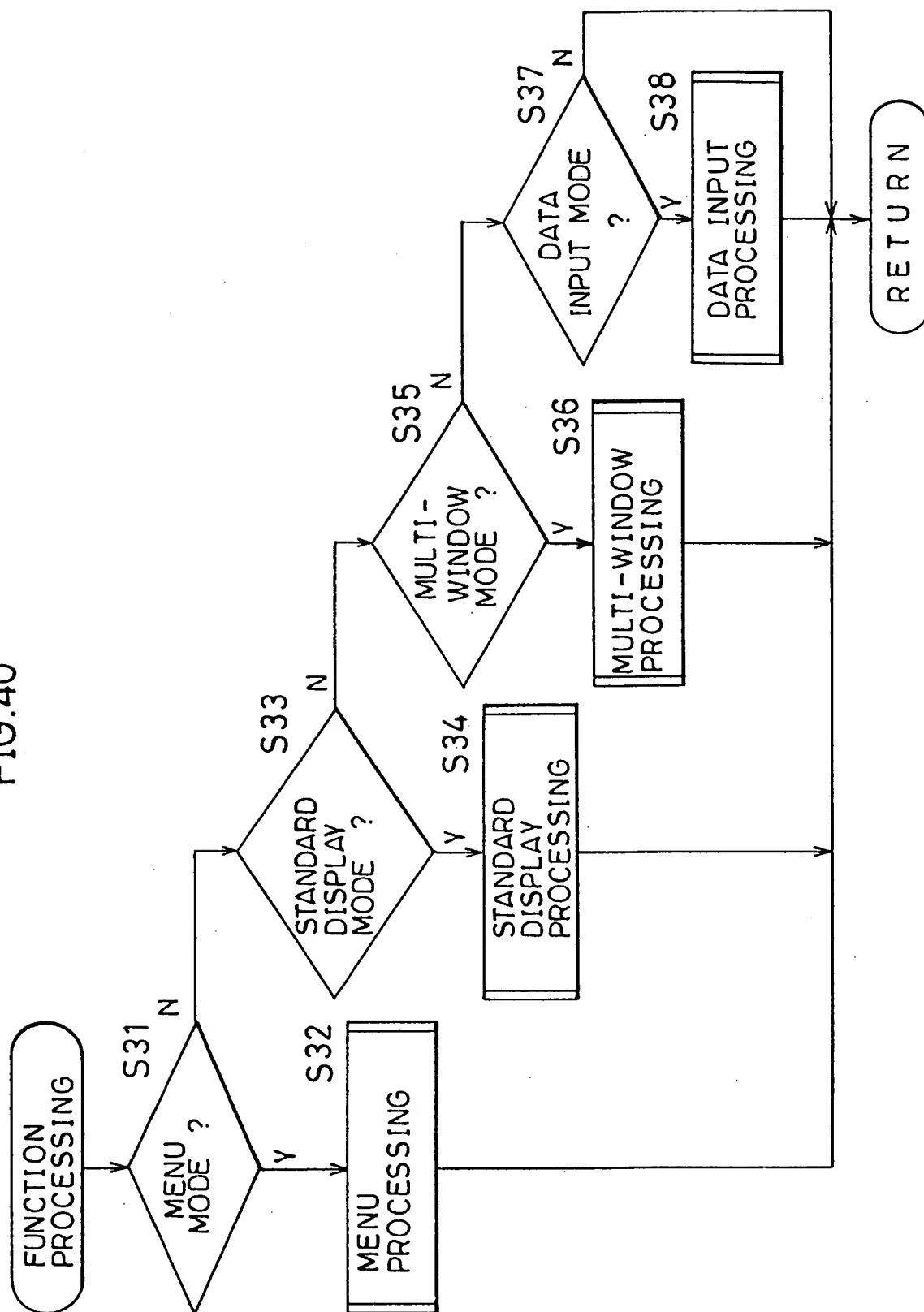


FIG. 41

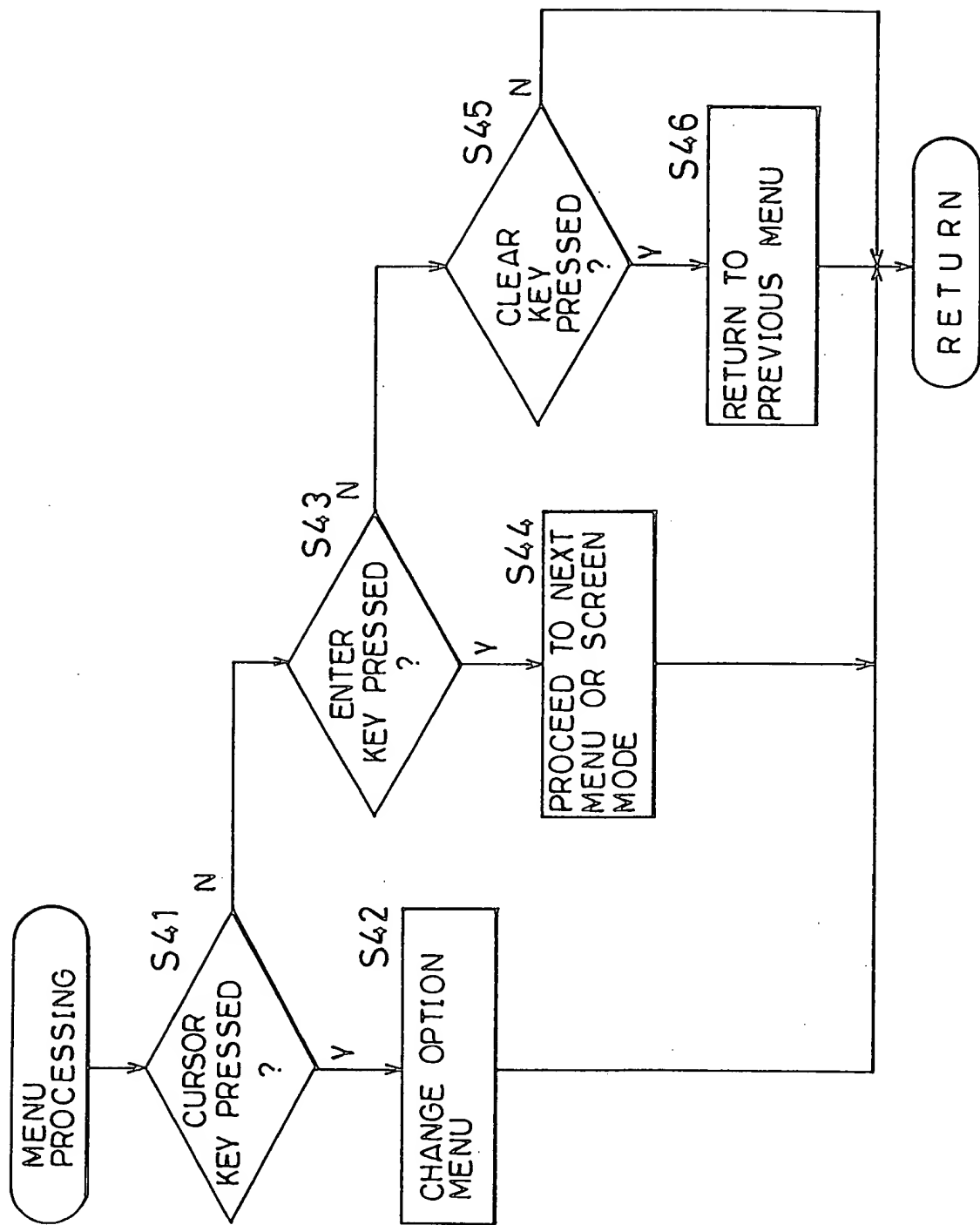


FIG.42

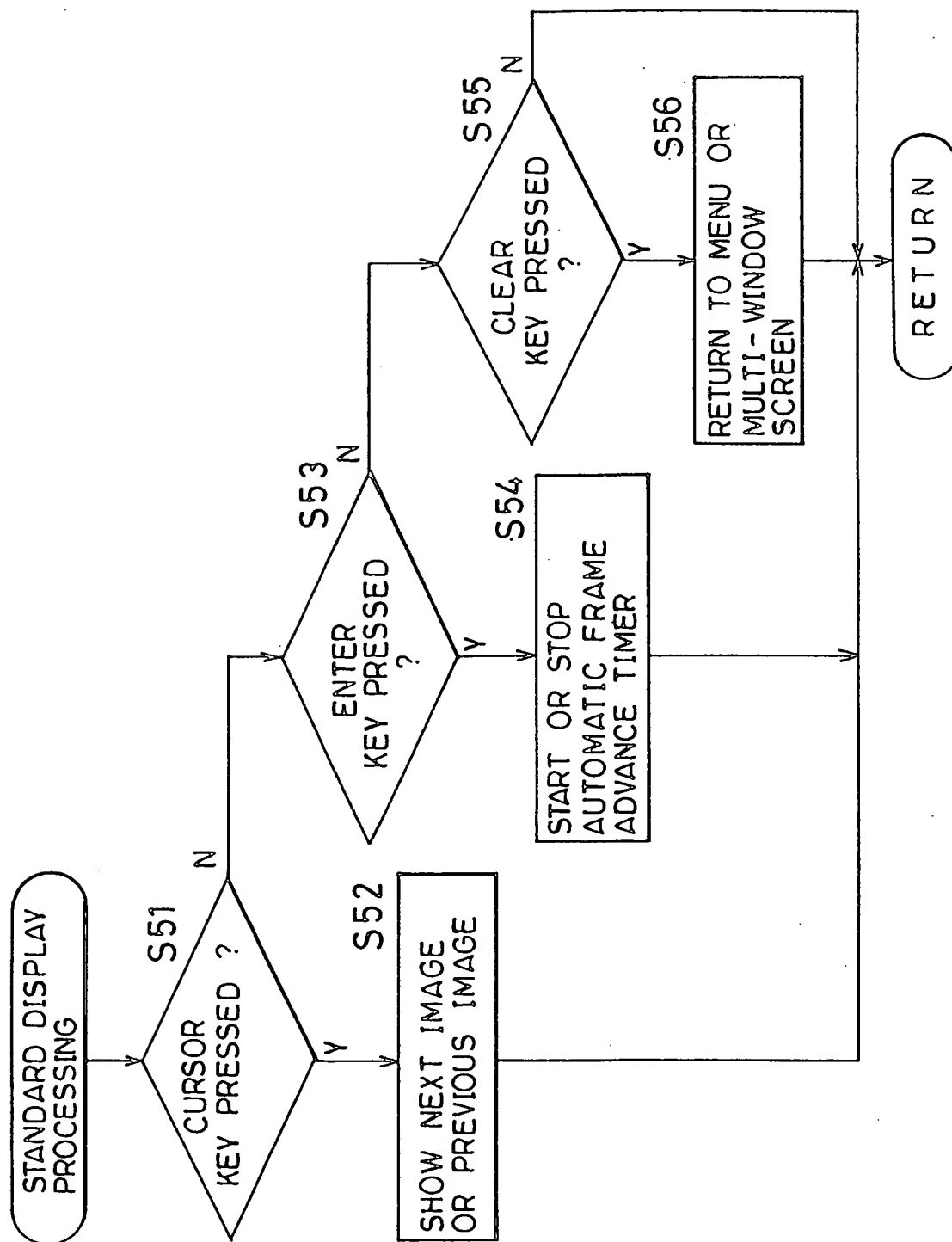


FIG.43

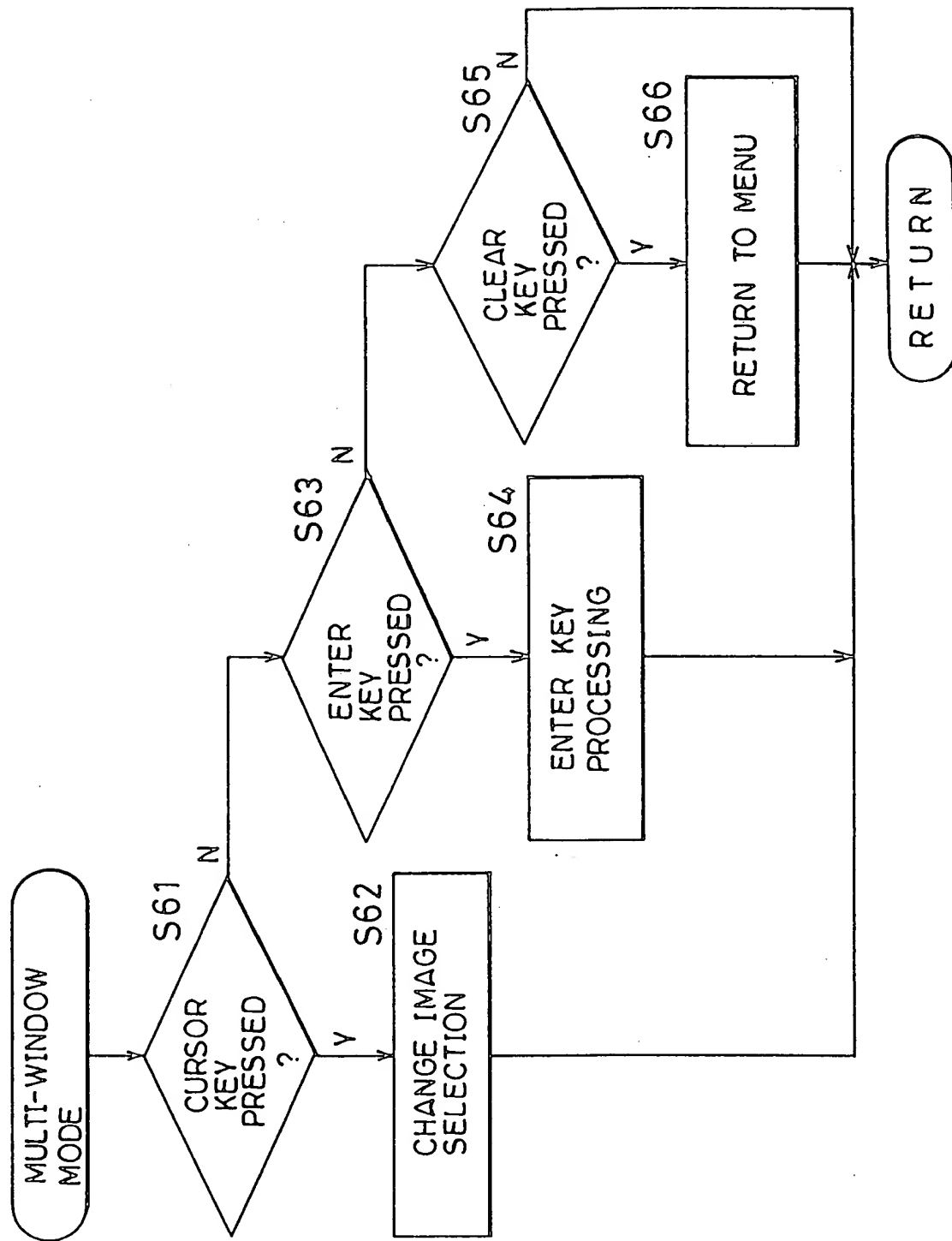


FIG.44

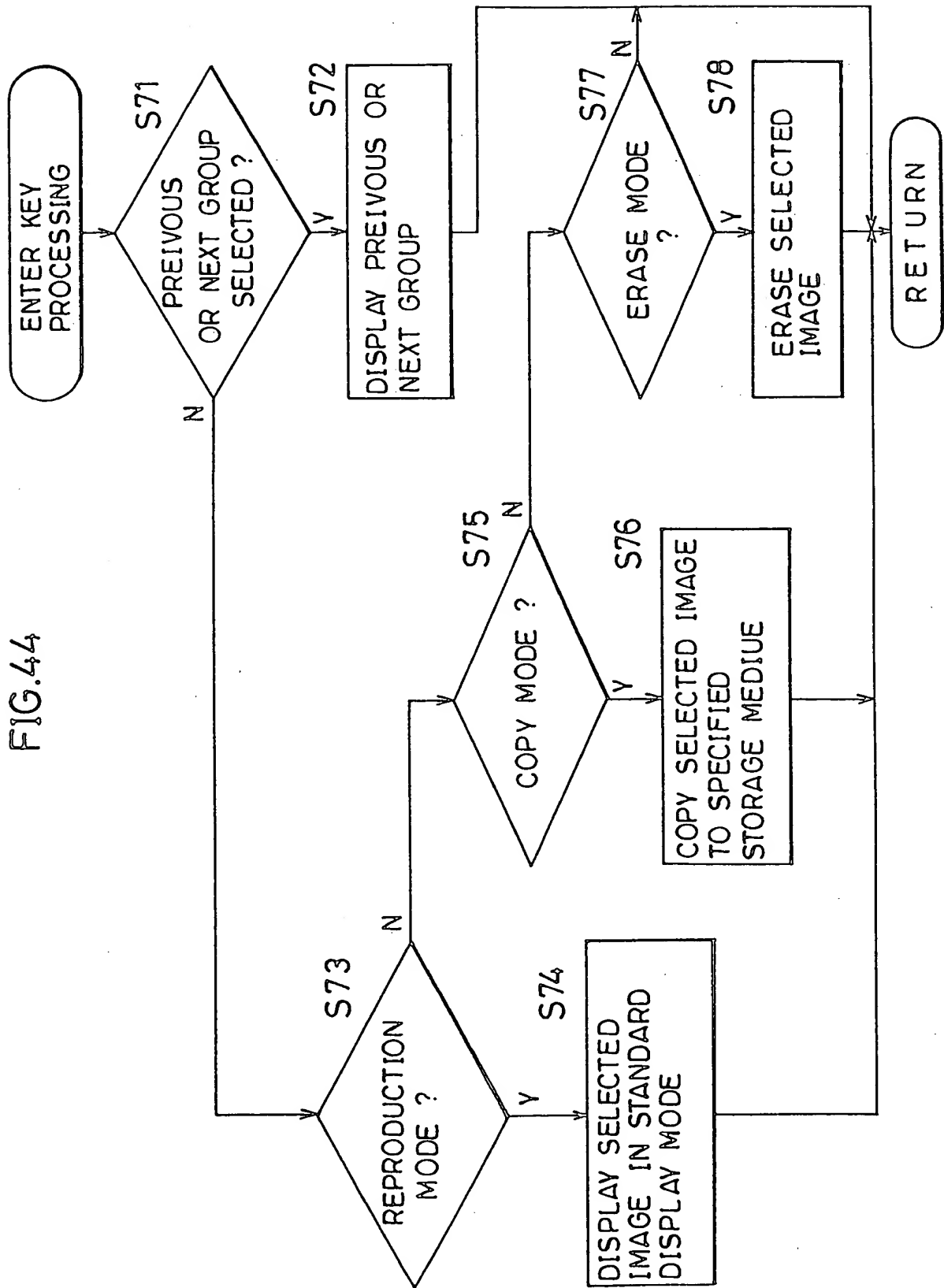


FIG.45

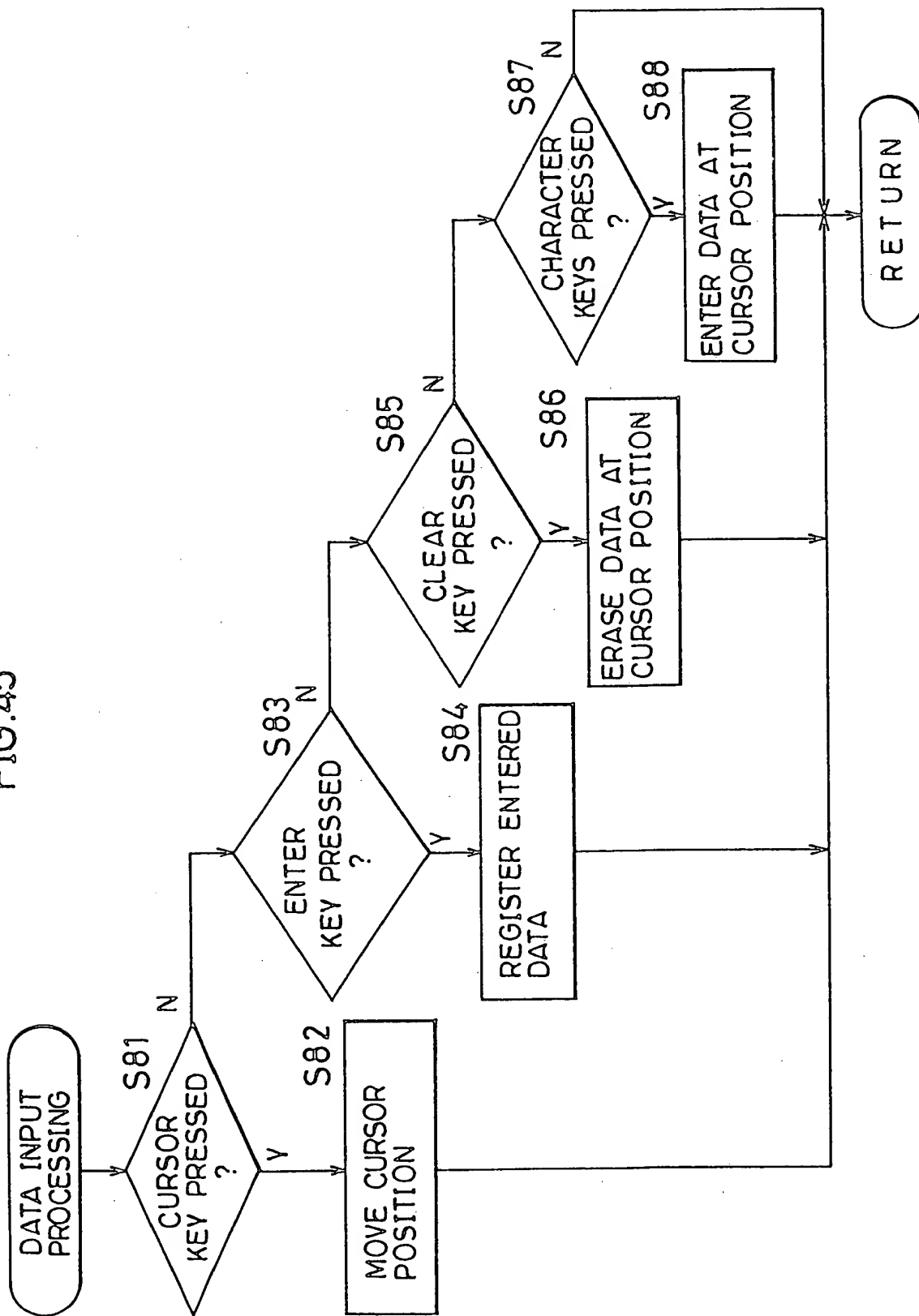


FIG.46

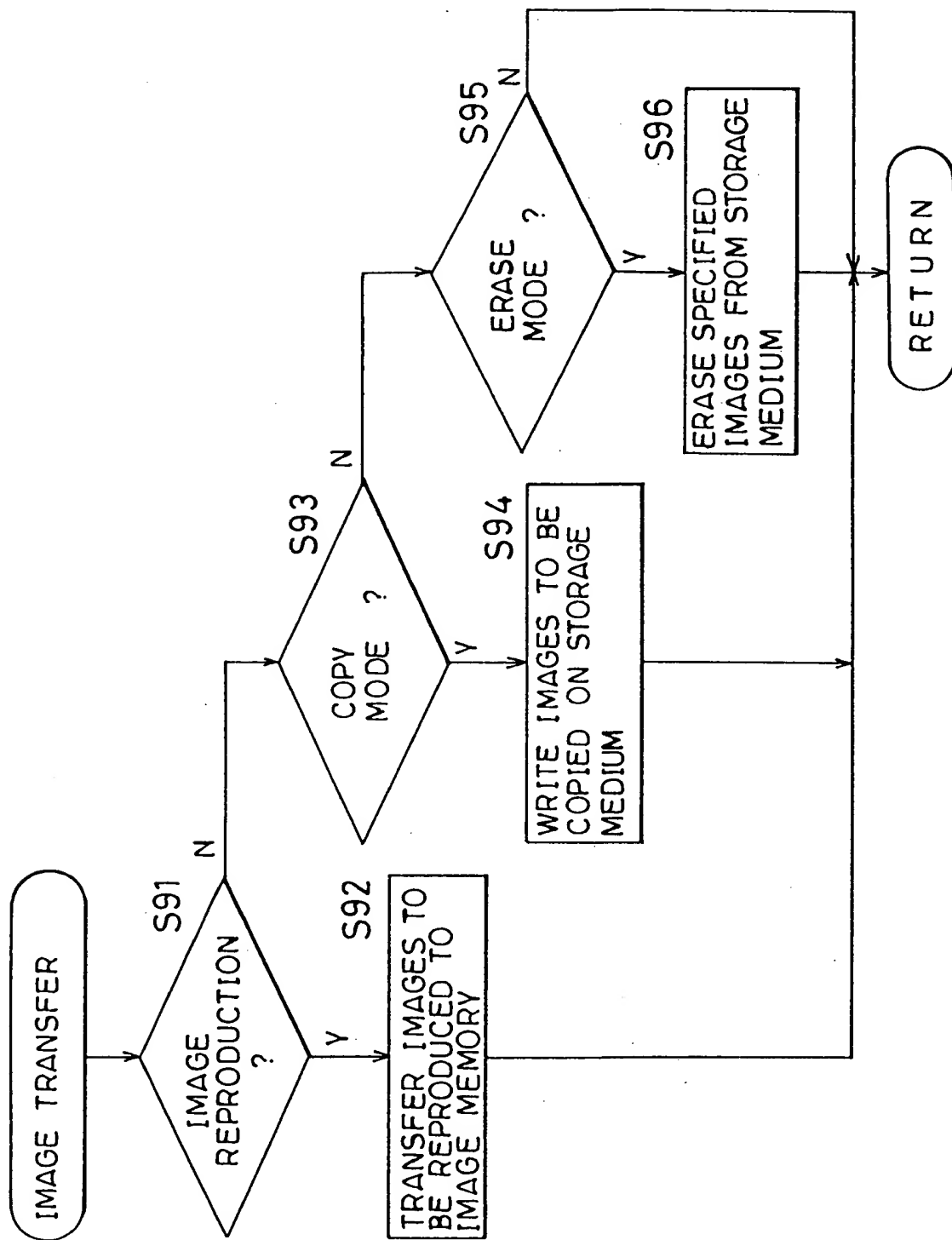


FIG.47

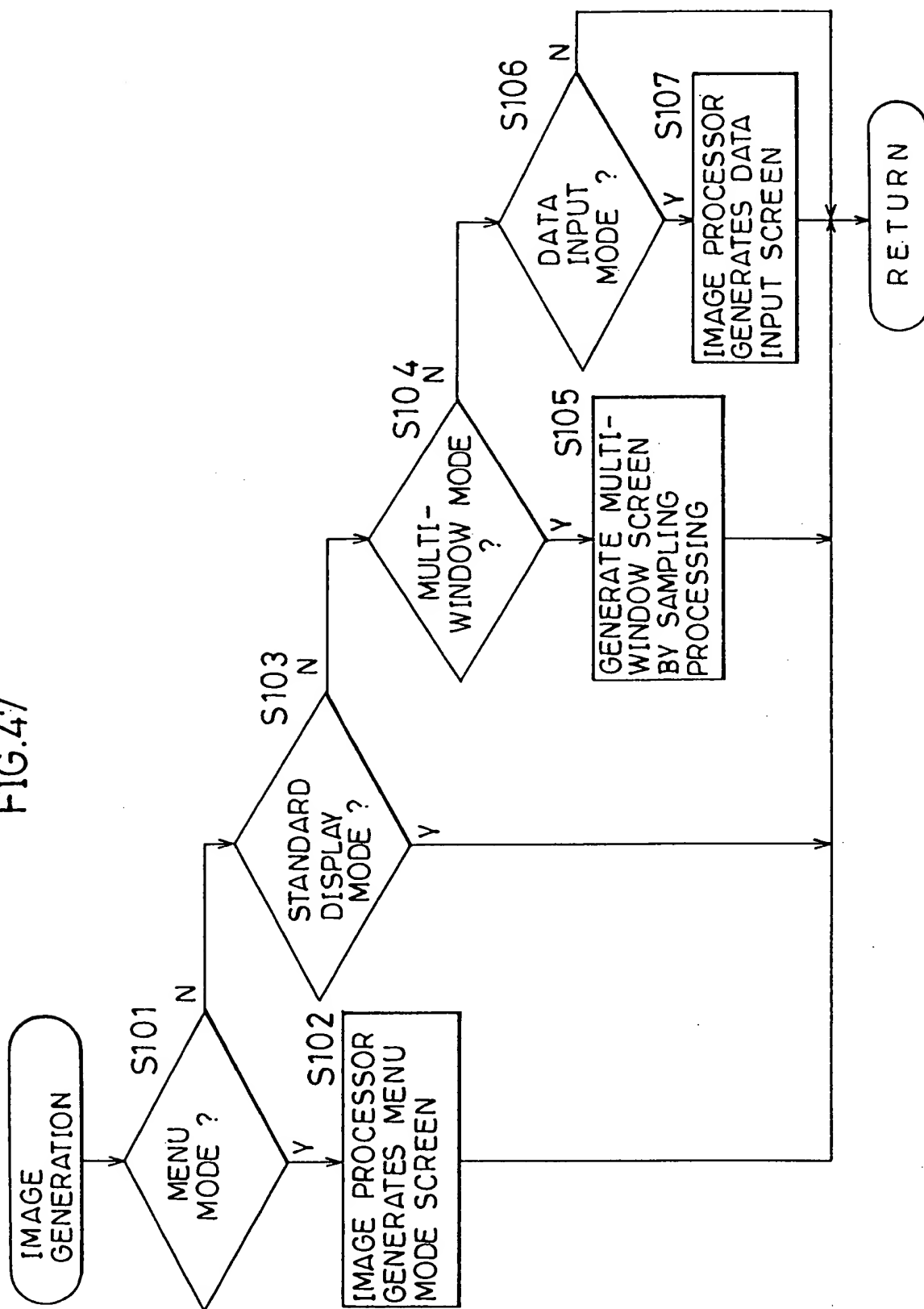


FIG.48

(a)

MENU

- **PLAYBACK**
- COPY
- ERASE
- OTHER FUNCTIONS

(b)

PLAYBACK

- **DISC**
- CARD 1
- CARD 2
- RANDOM

(c1)

PLAYBACK (DISC)

- **ALBUM 1 ()**
- ALBUM 2 ()
- ALBUM 3 ()
- ALBUM 4 ()
- ALBUM 5 ()

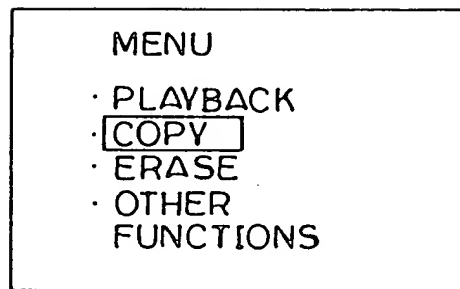
(c2)

PLAYBACK (CARD)

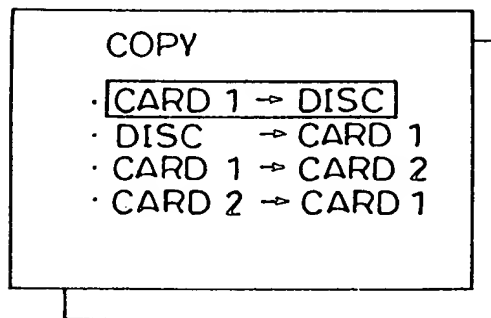
- **STANDARD DISPLAY**
- MULTI-WINDOW
- IMAGE SPECIFICATION
- RETRIEVAL

FIG.49

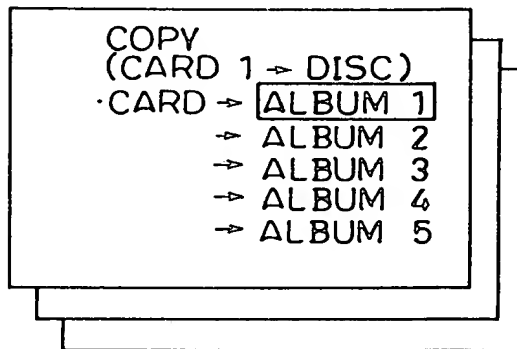
(a)



(b)



(c)



(d)

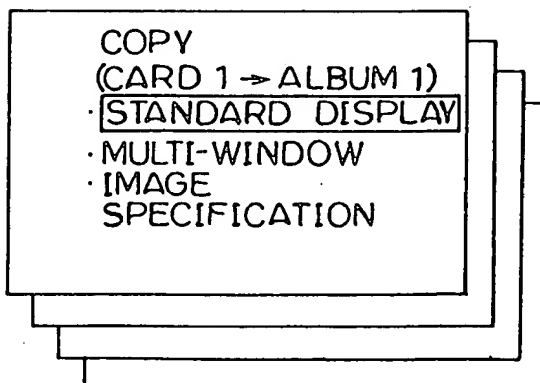


FIG.50

(a)

MENU

- PLAYBACK
- COPY
- **ERASE**
- OTHER FUNCTIONS

(b)

ERASE

- **CARD 1**
- CARD 2
- DISC

(c1)

ERASE (CARD 1)

- **STANDARD DISPLAY**
- MULTI-WINDOW
- IMAGE SPECIFICATION

(c2)

ERASE (DISC)

- **ALBUM 1**
- ALBUM 2()
- ALBUM 3()
- ALBUM 4()
- ALBUM 5()

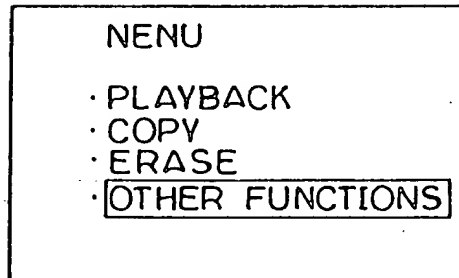
(d)

ERASE (ALBUM 1)

- **STANDARD DISPLAY**
- MULTI-WINDOW
- IMAGE SPECIFICATION

FIG.51

(a)



(b)

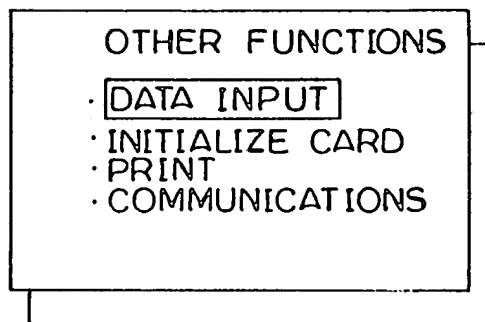


FIG.52

